

10/18/00  
JCS55 U.S. PTO

10/18/00

A

# UTILITY PATENT APPLICATION TRANSMITTAL

Address to: <b>Box PATENT APPLICATION</b> <b>Commissioner for Patents</b> <b>United States Patent &amp; Trademark Office</b> <b>Washington, D.C. 20231</b>	Attorney Docket No.	00-046-NSC
	Inventor(s) or Appln. Identifier	John W. Svenkeson and John D. Hamre

JCS55 U.S. PTO  
09/29/00  
10/18/00

1. This application entitled METHOD OF FORMING ALIGNMENT FEATURES FOR CONDUCTIVE DEVICES is:
- a. X A new application under 37 C.F.R. § 1.53(b).
- b.      A      continuation      divisional or      continuation-in-part application under 37 C.F.R. § 1.53(b) of prior application Serial No.      /      filed on     , entitled     .

Application elements and other attached papers:

2. X Specification (including Claims and Abstract) [Total Pages 32]
3. X Drawings (     informal / X formal) [Total Sheets 14]
4. X Oath or Declaration
- a. X Newly-executed
- b.      Copy from a prior application (37 C.F.R. § 1.63(d))
5.      Incorporation By Reference: The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Item 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6.      This application is filed by fewer than all the inventors named in the prior application, 37 C.F.R. § 1.53(d)(4).
- a.      **DELETE** the following inventor(s) named in the prior nonprovisional application:
- 
- 
- b.      The inventor(s) to be deleted are set forth on a separate sheet attached hereto.

CERTIFICATION UNDER 37 C.F.R. § 1.10	
I hereby certify that this UTILITY PATENT APPLICATION TRANSMITTAL and the documents referred to as attached therein are being deposited on the below date with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee" addressed to: Box Patent Application, Commissioner for Patents, United States Patent and Trademark Office, Washington, D.C. 20231 on:	
Express Mail Label No. <u>EK 211 157 384 US</u>	<u>STEPHANIE KLEPP</u> (Type or print name of person mailing paper)
Date of Deposit: <u>10/17/2000</u>	<u>[Signature]</u> (Signature of person mailing paper)

## 7. Preliminary Amendment:

- a. ☐ A Preliminary Amendment is attached.
- b. ☐ Cancel in this application original claims \_\_\_\_\_ of the prior application before calculating the filing fee.
- c. ☐ Please amend the specification by inserting before the first line the sentence:
- "This is a \_\_\_\_\_ continuation  
 \_\_\_\_\_ divisional  
 of copending application(s) serial number \_\_\_\_\_ / \_\_\_\_\_  
 filed on \_\_\_\_\_."
- d. ☐ A Petition to Suspend Prosecution For The Time Necessary to File An Amendment (New Application Filed Concurrently) is attached.

## 8. Small entity status:

- a. ☐ A small entity statement is attached.
- b. ☐ A small entity statement was filed in the prior nonprovisional application and such status is still proper and desired.
- c. ☐ Is no longer desired.

## 9. Fee Calculation:

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS (37 C.F.R. § 1.16(c))	12 -20 =	0	x 18.00	0
INDEPENDENT CLAIMS (37 C.F.R. § 1.16(b))	2 -3 =	0	x 80.00	0
MULTIPLE DEPENDENT CLAIMS (if applicable) (37 C.F.R. § 1.16(d))			270.00	0
BASIC FEE (37 C.F.R. § 1.16(a))				710.00
TOTAL OF ABOVE CALCULATIONS				710.00
REDUCTION BY 50% FOR FILING BY SMALL ENTITY (NOTE 37 C.F.R. §§ 1.9, 1.27, 1.28)				0
\$40.00 ASSIGNMENT RECORDAL FEE (if applicable)				40.00
<b>TOTAL</b>				<b>750.00</b>

10. ☐ A check in the amount of \$\_\_\_\_\_ is enclosed.
11. ☒ The Commissioner is hereby authorized to credit overpayments or charge the following fees (or any deficiency therein) to Storage Technology Corporation Deposit Account No. 19-4545 :
- a. ☒ Fees required under 37 C.F.R. § 1.16.
- b. ☒ Fees required under 37 C.F.R. § 1.17.

12. Maintenance of Copendency of Prior Application

       A request for extension of time and the appropriate fee have been filed in the pending **prior** application (or are being filed in the prior application concurrently herewith) to extend the period for response until \_\_\_\_\_.

13.   X   An Information Disclosure Statement (IDS) is attached, along with the following indicated attachments thereto:

a.   X   Form PTO/SB/08 (  1   sheet)

b.   X   Copies of references cited

14.        Certified copy of priority document(s)

15.   X   Return Receipt Postcard

16.        Other: \_\_\_\_\_  
\_\_\_\_\_

17.   X   An Assignment of the invention to Storage Technology Corporation

a.   X   is attached.

b.        was recorded on \_\_\_\_\_ at Reel \_\_\_\_\_, Frame \_\_\_\_\_.

18. The power of attorney in the prior application is to:

\_\_\_\_\_  
Name of Attorney of Record

\_\_\_\_\_  
Reg. No.

       The power appears in the original papers in the prior application.

       The power does not appear in the original papers, but was filed on \_\_\_\_\_.

       A new power has been executed and is attached.

19. Correspondence Address: Please address all future communications to:

Timothy R. Schulte,  
Storage Technology Corporation  
One StorageTek Drive, MS-4309  
Louisville, Colorado 80028-4309  
Telephone: 303-673-5989; Fax: 303-673-4151

Respectfully submitted,

Date 10/17/00



Name: Timothy R. Schulte  
Registration No.: 29,013

  X   Attorney or agent of record  
       Filed under Rule 34(a)

## METHOD OF FORMING ALIGNMENT FEATURES FOR CONDUCTIVE DEVICES

### TECHNICAL FIELD

5 The invention relates to an improved method for forming alignment features on conductive devices.

### BACKGROUND ART

10 An important and continuing goal in the computer and electronics industries is that of increasing conductive or signal trace density and bandwidth of conductive devices, such as printed circuit boards (PCB's). A limiting factor for increasing the trace density is the ability to properly align the traces of one PCB with the traces of another PCB when the PCB's are electrically joined together. In order to align the traces of the PCB's, alignment features are typically formed on each PCB.

15 A prior method of forming a PCB with such alignment features includes forming a plurality of traces on a substrate. The PCB is then inserted into a drilling device, and a drill bit of the drilling device is optically aligned with contact portions of the traces. Next, the drill bit is used to form a plurality of mechanical alignment features such as holes in the board. The holes may then be used to pin the  
20 PCB to another PCB having similar holes so as to align the contact portions of the PCB's.

Because the mechanical alignment features are formed separately from the traces, it is difficult to obtain precise registration of the mechanical alignment features with the contact portions of the traces.

25

## DISCLOSURE OF INVENTION

The invention addresses the shortcomings of the prior art by providing an improved method of forming a conductive device having one or more mechanical alignment features that are aligned with conductive traces of the device.

5

Under the invention, a method of forming a conductive device includes forming a conductive layer on a substrate; etching the conductive layer to form a plurality of conductive traces; etching the conductive layer to form at least one mask feature; and removing substrate material that is not covered by the at least one mask feature so as to form at least one mechanical alignment feature.

10

Preferably, the forming steps are performed simultaneously by a photo-etching process. As a result, precise registration between the at least one mask feature and the contact portions can be achieved.

15

Advantageously, the method of the invention may be used to form various configurations of the at least one alignment feature. For example, the at least one alignment feature may be formed as a hole, a side edge, a tab and/or a slot.

20

More specifically, a method under the invention of forming a printed circuit board includes forming a conductive layer on a substrate; etching the conductive layer to form multiple conductive traces, each trace having a contact portion; etching the conductive layer to form multiple mask features that cooperate to define a template; and ablating with a laser substrate material that is not covered by the template so as to form a plurality of mechanical alignment features.

25

Theses and other objects, features and advantages of the invention are readily apparent from the following detailed description of the best modes for carrying out the invention when taken in conjunction with the accompanying drawings.

## BRIEF DESCRIPTION OF DRAWINGS

FIGURE 1 is a perspective view of a first embodiment of a conductive network according to the invention including first and second multi-layer printed circuit boards and a connector assembly, wherein the connector assembly  
5 includes first and second plug assemblies and a socket assembly;

FIGURE 2 is an exploded perspective view of the plug assemblies and the socket assembly;

FIGURE 3 is a side cross-sectional view of the conductive network showing the plug assemblies engaged with the socket assembly;

10 FIGURE 4 is an enlarged fragmentary view of a portion of Figure 3;

FIGURE 5 is a plan view of a first signal layer of the first printed circuit board;

FIGURE 6 is a bottom view of the first signal layer of the first printed circuit board;

15 FIGURE 7 is a plan view of a second signal layer of the first printed circuit board;

FIGURE 8 is a bottom view of the second signal layer of the second printed circuit board;

20 FIGURE 9 is a perspective view of a bridge pad of the socket assembly;

FIGURE 10 is a fragmentary perspective view of the bridge pad of Figure 9;

FIGURE 11 is a fragmentary plan view of the bridge pad of Figure 9;

FIGURE 12 is a perspective view of a second embodiment of the conductive network according to the invention including first and second multi-layer printed circuit boards and a connector assembly, wherein the connector assembly includes first and second fixtures and a plurality of fasteners;

FIGURE 13 is a cross-sectional view of the second embodiment of the conductive network with the first and second fixtures spaced away from each other;

FIGURE 14 is a cross-sectional view of the second embodiment of the conductive network with the first and second fixtures engaged with each other;

FIGURE 15 is an enlarged fragmentary cross-sectional view of the first and second printed circuit boards of the second embodiment of the conductive network positioned adjacent each other;

FIGURE 16 is a fragmentary plan view of a first signal layer of the first printed circuit board of the second embodiment of the conductive network;

FIGURE 17 is a fragmentary plan view of a first bridge layer of the first printed circuit board of the second embodiment of the conductive network;

FIGURE 18 is a fragmentary plan view of a second signal layer of the second printed circuit board of the second embodiment of the conductive network;

FIGURE 19 is a fragmentary plan view of a second bridge layer of the second printed circuit board of the second embodiment of the conductive network;

FIGURE 20 is a perspective view of a third embodiment of the conductive network according to the invention including first and second printed

circuit boards and a connector assembly, wherein the connector assembly includes first and second fixtures;

FIGURE 21 is a side view of the third embodiment of the conductive network;

5                   FIGURE 22 is a cross-sectional view of the third embodiment of the conductive network;

10                  FIGURE 23 is a perspective view of the printed circuit boards of the third embodiment of the conductive network with the first and second fixtures removed;

FIGURE 24 is a bottom perspective view of the printed circuit boards of Figure 23;

FIGURE 25 is an end cross-sectional view of the printed circuit boards of Figure 23; and

15                  FIGURE 26 is a fragmentary perspective view of a fourth embodiment of the conductive network according to the invention.

### **BEST MODES FOR CARRYING OUT THE INVENTION**

20                  Figures 1-4 show a conductive system or network 10 for use in computer and/or electronic equipment, such as data processing equipment and networking equipment. The network 10 includes first and second conductive devices, such as first and second multi-layer printed circuit boards (PCB's) 12 and 14, respectively, and a connector assembly 16 for connecting together the PCB's 12 and 14. The first PCB 12 has a first main body 18 that includes multiple first signal layers 20. A portion 21 of each first signal layer 20 preferably extends beyond the  
25                  first main body 18 so as to provide access to each first signal layer 20. Furthermore,



the portions 21 are preferably flexible and not bonded to each other proximate distal ends of the portions 21, so that the portions 21 are independently moveable.

Referring to Figures 5 and 6, each first signal layer 20 has a first substrate 22, a plurality of first conductive paths or traces 24 disposed on one side of the first substrate 22, and a first ground plane 26 disposed on an opposite side of the first substrate 22. While the first substrates 22 may comprise any suitable material such as an insulating polymer, each first substrate 22 preferably comprises HBR Flex™, available from Hadco Corp. of Salem, New Hampshire. Other suitable materials include MYLAR™.

Each first trace 24 includes a first contact portion 28. Preferably, first substrate material is removed between adjacent first contact portions 28 so as to form a plurality of first apertures such as first gaps or notches 29. With such a configuration, each first contact portion 28 may be independently displaced with respect to the other first contact portions 28. Alternatively, first substrate material may be removed between select first contact portions 28, or the first substrate 22 may be left intact.

Each first ground plane 26 may have any suitable configuration such as a solid plane shown in Figure 6, or a cross-hatched configuration as is known in the art. Each first ground plane 26 also includes a plurality of first ground contact portions 30. Advantageously, the first ground contact portions 30 are also separated by the first notches 29 so that each first ground contact portion 30 may be independently displaced with respect to the other first ground contact portions 30.

Each first signal layer 20 also includes one or more first alignment features, such as first holes 31 and first side edges 32 as shown in Figure 5. The first alignment features are used to align the first contact portions 28 with respect to other elements, as explained below in greater detail. The first alignment features are preferably formed by a novel process described below, so as to provide precise registration of the first alignment features with respect to the first contact portions 28.

Similar to the first PCB 12, the second PCB 14 has a second main body 33 that includes multiple second signal layers 34 as shown in Figures 1 and 2. A portion 35 of each second signal layer 34 preferably extends beyond the second main body 33 so as to provide access to each second signal layer 34. Furthermore, the portions 35 are preferably flexible and not bonded to each other proximate distal ends of the portions 35, as shown in Figure 2, so that the portions 35 are independently moveable.

Referring to Figures 7 and 8, each second signal layer 34 has a second substrate 36, a plurality of second conductive paths or traces 38 disposed on one side of the second substrate 36, and a second ground plane 40 disposed on an opposite side of the second substrate 36. Each second trace 38 includes a second contact portion 42. Preferably, second substrate material is removed between adjacent second contact portions 42 so as to form a plurality of second apertures such as second gaps or notches 43. With such a configuration, each second contact portion 42 may be independently displaced with respect to the other second contact portions 42. Alternatively, second substrate material may be removed between select second contact portions 42, or the second substrate 36 may be left intact.

Each second ground plane 40 may have any suitable configuration such as a cross-hatched configuration shown in Figure 8, or a solid plane. Each second ground plane 40 also includes a plurality of second ground contact portions 44. Advantageously, the second ground contact portions 44 are also separated by the second notches 43 so that each second ground contact portion 44 may be independently displaced with respect to the other second ground contact portions 44.

Each second signal layer 34 also includes one or more second alignment features, such as second holes 45 and second side edges 46. The second alignment features are used to align the second contact portions 42 with respect to other elements, as explained below in greater detail. The second alignment features are preferably formed by a novel process described below, so as to provide precise registration of the second alignment features with respect to the second contact portions 42.

Referring to Figures 1-3, the connector assembly 16 is used to electrically join together the first and second contact portions 28 and 42, respectively, as well as the first and second ground planes 26 and 40, respectively. The connector assembly 16 includes multiple fixtures that are joinable together. For example, the connector assembly 16 may include first and second spacer assemblies such as first and second plug assemblies 47 and 48, respectively, and a bridging assembly such as a receptacle or socket assembly 50. Alternatively, the spacer assemblies may be configured as socket assemblies, and the bridging assembly may be configured as a plug assembly. The connector assembly 16 further includes a clamping device 51, as shown in Figures 1 and 3.

The first plug assembly 47 is connected to the first PCB 12, and includes a first housing 52 that receives the first signal layers 20. The first plug assembly 47 further includes a plurality of first spacers 53, and each first spacer 53 is disposed between adjacent first signal layers 20, or between a first signal layer 20 and the first housing 52, for spacing the first signal layers 20 apart. Each first spacer 53 includes a plurality of first spacer holes 54 that are aligned with the first holes 31 of the first signal layers 20. While the first spacers 53 may comprise any suitable material, the first spacers 53 preferably comprise a non-conductive polymer.

One or more first alignment members, such as first pins 55, extend through the first holes 31 and the first spacer holes 54, so as to align the first signal layers 20 with respect to each other and with respect to the first spacers 53. The first pins 55 also connect the first signal layers 20 to the first housing 52.

Alternatively, in lieu of the first pins 55 and the first spacer holes 54, each first spacer 53 may be provided with a plurality of projections on one side that extend through the first holes 31 of a particular first signal layer 20, and a plurality of recesses on an opposite side for receiving the projections of another first spacer 53. With such a configuration, the first spacers 53 may be snap fit together with each first signal layer 20 being sandwiched between two first spacers 53. The first housing 52 may also be provided with additional projections on one interior surface and additional recesses on another interior surface, so that one first spacer 53 may

be snap fit onto the additional projections and another first spacer 53 may be snap fit into the additional recesses.

5 The second plug assembly 48 is connected to the second PCB 14 and includes a second housing 56 that receives the second signal layers 34. The second plug assembly 48 further includes a plurality of second spacers 57, and each second spacer 57 is disposed between adjacent second signal layers 34, or between a second signal layer 34 and the second housing 56, for spacing the second signal layers 34 apart. Each second spacer 57 includes a plurality of second spacer holes 58 that are  
10 aligned with the second holes 45 of the second signal layers 34. While the second spacers 57 may comprise any suitable material, the second spacers 57 preferably comprise a non-conductive polymer.

One or more second alignment members, such as second pins 59, extend through the second holes 45 and the second spacer holes 58, so as to align the  
15 second signal layers 34 with respect to each other and with respect to the second spacers 57. The second pins 59 also connect the second signal layers 34 to the second housing 56.

Alternatively, in lieu of the second pins 59 and the second spacer holes 58, each second spacer 57 may be provided with a plurality of projections on  
20 one side that extend through the second holes 45 of a particular second signal layer 34, and a plurality of recesses on an opposite side for receiving the projections of another second spacer 57. With such a configuration, the second spacers 57 may be snap fit together with each second signal layer 34 being sandwiched between two second spacers 57. The second housing 56 may also be provided with additional  
25 projections on one interior surface and additional recesses on another interior surface, so that one second spacer 57 may be snap fit onto the additional projections and another second spacer 57 may be snap fit into the additional recesses.

The socket assembly 50 is releasably engageable with the plug  
30 assemblies 47 and 48. The socket assembly 50 includes a third housing, such as a socket housing 60, having first and second openings 62 and 64, respectively. A

plurality of bridge layers such as bridge pads 66 are disposed in the socket housing 60 for electrically bridging the signal layers 20 and 34. Referring to Figures 9-11, each bridge pad 66 includes a bridge substrate 67 and a plurality of bridge traces 68 disposed on one side of the bridge substrate 67. Each bridge trace 68 has first and second bridge contact portions 70 and 72, respectively. Preferably, bridge substrate material is removed between adjacent bridge contact portions 70 and 72 so as to form a plurality of apertures 74. With such a configuration, each bridge contact portion 70 and 72 may be independently displaced with respect to the other bridge contact portions 70 and 72.

Each bridge contact portion 70 and 72 may have any suitable configuration, such as a U-shaped configuration or a rectangular configuration. In the embodiment shown in Figures 9-11, each bridge contact portion 70 and 72 has a fish-bone configuration that includes a longitudinally extending main section and a plurality of laterally extending projections extending from the main section. Preferably, each aperture 74 extends between the laterally extending projections of a particular bridge contact portion 70 and 72, so that each laterally extending projection may also be independently displaced.

Referring to Figures 2 and 9, the socket assembly 50 further includes a plurality of separators 76, a plurality of fill pads 77, and one or more alignment members such as rollers 78. The separators 76 cooperate with the bridge pads 66 to separate select bridge pads 66 from each other so as to receive select signal layers 20 and 34 therebetween. The fill pads 77 preferably comprise an elastomer or other suitable material, and function to concentrate clamping forces on the contact portions 28, 30, 42, 44, 70 and 72, as explained below in greater detail. The third spacers 77 also allow for the independent displacement of the contact portions 28, 30, 42, 44, 70 and 72. The rollers 78 are engageable with side edges 80 of each bridge pad 66 so as to properly align the bridge pads 66 within the socket housing 60.

Alternatively, the socket assembly 50 may be provided with bridge layers that each include a bridge substrate and a plurality of bridge traces disposed on opposite sides of the bridge substrate. With such a configuration, a single bridge

layer could be used to electrically join together two pairs of mating signal layers. For example, such a bridge layer could be used to electrically join first signal traces 24 of each of two first signal layers 20 with second signal traces 38 of each of two second signal layers 34.

5 Referring to Figures 1, 3 and 4, the clamping device 51 cooperates with the socket assembly 50 to force together the bridge pads 66 and the signal layers 20 and 34 such that a first group of bridge pads 66 electrically joins the first and second traces 24 and 38, respectively, and a second group of bridge pads 66 electrically joins the first and second ground planes 26 and 40, respectively. The  
10 clamping device 51 includes first and second clamp sections 82 and 84, respectively, that are attached to the first and second housings 53 and 56, respectively, such that the first and second clamp sections 82 and 84, respectively, can move with respect to the first and second housings 53 and 56, respectively. The first clamp section 82 has a pair of engaging portions 86 that are engageable with a pair of engaging  
15 portions 88 of the second clamp section 84 so as to exert a clamping force on the bridge pads 66 and signal layers 20 and 34, through the openings 62 and 64. Alternatively, the clamping device 52 may have any suitable configuration for exerting a sufficient clamping force on the bridge pads 66 and signal layers 20 and 34.

20 A method according to the invention for manufacturing the PCB's 12 and 14, and for assembling the conductive network 10 will now be described. Because the PCB's 12 and 14 may be made in a similar manner, this detailed description will focus primarily on the method of making the first PCB 12. However, a thorough understanding of the method of making the second PCB 14 will  
25 be apparent therefrom. Each first signal layer 20 of the first PCB 12 is preferably made by a photo-etching process followed by an ablation process, as explained below in detail.

Referring to Figures 5 and 6, conductive films or foils, such as copper foils or copper-alloy foils, are first applied to both sides of a particular first substrate  
30 22, as is known in the art. Next, a substance that hardens when exposed to

ultraviolet light, such as dry film resist, is preferably applied over the foils. A pattern, commonly referred to as artwork, is then placed over the dry film resist on each side of the substrate 22. Each pattern covers portions of a particular foil that are to be removed such as through chemical etching. In accordance with an aspect of the invention, the pattern placed on one side of the first substrate 22 preferably defines the outlines or outer boundaries of the first traces 24, as well as outer boundaries of first guide features or first mask features 90 to be used in forming the first alignment features, such as the first holes 31 and first side edges 32. The pattern placed on the opposite side of the first substrate 22 defines the outline of the first ground plane 26, including the first ground contact portions 30.

Next, sections of the dry film resist not covered by the patterns are exposed to ultraviolet light so as to harden such sections. Non-hardened sections of the dry film resist are then removed through a chemical process known as developing. Next, portions of the foils not covered by hardened dry film resist are removed, such as by chemical etching, so as to define the first traces 24 and first mask features 90 on the one side of the first substrate 22, and to define the ground plane 26 on the opposite side of the first substrate 22.

The first mask features 90 cooperate to define a cutting template that defines outer boundaries of the first alignment features. Next, a soft laser, i.e., a laser that will not remove or ablate the foils, is used to ablate first substrate material that is not in the shadow of or otherwise covered by the template so as to define the first alignment features. The beam of the laser is preferably positioned normal to the first substrate 22 so that the template may be used to accurately define the first alignment features. Alternatively, first substrate material may be removed by plasma ablation or any other suitable ablation process.

Because the first mask features 90 are formed simultaneously with the first traces 24, the method of the invention provides precise registration of the first alignment features with the first traces 24. For example, tolerances of  $\pm 0.025$  millimeters (mm) may be achieved between each first hole 31 and each first contact portion 28. Advantageously, with such a process, tolerances between the first

contact portions 28 and the first alignment features is reduced to tolerances achievable through the photo-etching process.

Alternatively or supplementally, first alignment features may be formed in the same manner for the first ground contact portions 30 of the first ground plane 26. With this arrangement, first mask features would need to be formed on the same side of the first substrate 22 as the first ground contact portions 30, so as to provide precise registration of such first mask features with the first ground contact portions 30.

The laser, or other suitable device, is also preferably used to remove first substrate material disposed between the first contact portions 28, so as to form the first notches 29. During this process, the first contact portions 28 function as mask features or templates for the first notches 29.

After a plurality of first signal layers 20 have been formed, the first signal layers 20 may be bonded together and cut to a desired size and shape so as to form the first PCB 12 having the first main body 18 and portions 21 that extend from the first main body 18. As previously mentioned, however, the portions 21 are preferably not bonded to each other proximate distal ends of the portions 21. Alternatively, the first signal layers 20 may be cut or otherwise shaped prior to bonding the first signal layers 20 together.

20

Returning to Figures 1 and 2, the portions 21 of the first PCB 12 are then positioned between top and bottom pieces of the first housing 52 of the first plug assembly 47, along with the first spacers 53. The first pins 55 are then inserted through the first holes 31 and the first spacer holes 54, and engaged with the first housing 52, so as to properly align the first signal layers 20 within the first housing 52. The two pieces of the first housing 52 are then preferably snap fit together. Alternatively, the pieces of the first housing 52 may be connected together in any suitable manner such as with one or more fasteners or an adhesive.

25



As explained above, the second PCB 14 may be manufactured in a similar manner as the first PCB 12. The second PCB 14 may also be connected to the second plug assembly 48 in a similar manner as described above with respect to the first PCB 12 and the first plug assembly 47.

5                   The bridge pads 66 of the socket assembly 50 are also preferably manufactured in a similar manner as the first signal layers 20 of the first PCB 12. More specifically, referring to Figures 9-11, the bridge traces 68 may be formed on one side of each bridge substrate 67 by the photo-etching process described above in detail. Similarly, guide features or mask features for forming the side edges 80  
10                   may also be formed by the photo-etching process. Bridge substrate material is then preferably removed from each bridge substrate 67 by laser ablation, or other suitable ablation process, so as to form the apertures 74 and to define the side edges 80, which function as alignment features for the bridge layers 66.

                  Each bridge pad 66 is then shaped or otherwise formed, such as by  
15                   folding, to achieve a desired configuration. Each bridge pad 66 is preferably formed so as to have a cavity 91 for receiving a separator 76 and a fill pad 77. Furthermore, each bridge pad 66 may be formed around a separator 76 and a fill pad 77, or a separator 76 and a fill pad 77 may be inserted into each bridge pad 66 after each bridge pad 66 has been formed into the desired configuration.

20                   While each separator 76 may be any suitable device that is able to be compressed together under sufficient pressure, each separator 76 is preferably a stamped metal spring or other suitable spring. Each bridge pad 66 may also be provided with a conductive foil 92, or other suitable layer, on the side of the corresponding bridge substrate 67 opposite the bridge traces 68. Such a foil assists  
25                   in maintaining the desired configuration of the bridge pad 66.

                  Returning to Figures 1 and 2, the bridge pads 66 are then positioned between top and bottom pieces of the socket housing 60 so that the bridge traces 68 of adjacent bridge pads 66 face each other. The rollers 78 are then engaged with the side edges 80 of the bridge pads 66 so as to align the bridge pads within the housing

60. The two pieces of the socket housing 60 are then preferably snap fit together. Alternatively, the pieces of the socket housing 60 may be connected together in any suitable manner such as with one or more fasteners or an adhesive. The socket housing 60 may then be mounted in a panel or other member that supports the socket assembly 50.

Referring to Figures 1, 3 and 4, the first and second plug assemblies 47 and 48 may then be inserted into or otherwise engaged with the socket assembly 50, such that first and second openings 93 and 94, respectively, of each plug assembly 47 and 48 are respectively aligned with the first and second openings 62 and 64, respectively, of the socket assembly 50. Next, the first and second clamp portions 82 and 84 may be slid or otherwise moved toward each other, until the engaging portions 86 and 88 are engaged with each other, so as to apply the clamping force on the bridge pads 66 and signal layers 20 and 34. Advantageously, the assemblies 47, 48 and 50 allow for blind joining of the PCB's 12 and 14 if, for example, the socket assembly 50 is mounted in a panel or other support member.

The fill pads 77 function to concentrate the clamping force at the contact portions 28, 30, 42, 44, 70 and 72, thereby improving the connection between the PCB's 12 and 14. Advantageously, because the fill pads 77 preferably comprise an elastomer or other suitable flexible material, the fill pads 77 are able to conform to surface variations, or non-coplanarity, of the signal layers 20 and 34 and bridge pads 66 so as to maintain an even distribution of the clamping force over the contact portions 28, 30, 42, 44, 70 and 72.

Moreover, because each contact portion 28, 30, 42, 44, 70 and 72 is preferably independently moveable, the conductive network 10 is able to overcome any non-coplanarity of the signal layers 20 and 34, as well as any non-coplanarity of the bridge pads 66 proximate the bridge traces 68. More specifically, each contact portion 28, 30, 42, 44, 70 and 72 may be independently displaced so as to provide maximum contact with a respective mating contact portion 28, 30, 42, 44, 70 and 72. As a result, the conductive network 10 provides optimum electrical joining of the PCB's 12 and 14.

While the figures show that substrate material is preferably removed between each contact portion 28, 30, 42, 44, 70 and 72, substrate material may be removed between select contact portions 28, 30, 42, 44, 70 and 72. For example, substrate material may be removed between groups of two contact portions 28 or groups of three contact portions 28.

Advantageously, each contact portion 28, 30, 42, 44, 70 and 72 can be formed with a configuration that is sufficiently fine, in terms of contact portion width and sharpness of contact portion edges, so as to create localized areas of relatively high stress concentration when the contact portions 28, 30, 42 and 44 are aligned and forced together with mating contact portions 70 and 72. These areas of high stress concentration deform the contact portions 28, 30, 42, 44, 70 and 72 so as to create reliable connections between mating contact portions 28, 30, 42, 44, 70 and 72, while minimizing clamping forces required to make such connections. Preferably, each contact portion 28, 30, 42, 44, 70 and 72 is formed with a width in the range of 1 to 2 mm, or less. Furthermore, the edges of the contact portions 28, 30, 42, 44, 70 and 72 are preferably formed with a radius less than 0.1mm.

Figures 12-14 show a second embodiment 110 of the conductive network that includes first and second conductive devices, such as first and second multi-layer PCB's 112 and 114, respectively, and a connector assembly 116 for connecting together the PCB's 112 and 114. The first PCB 112 has a first main body 118 that includes multiple first signal layers 120 that alternate with first bridge layers 121. A portion 122 of each first signal layer 120 and a portion 123 of each first bridge layer 121 preferably extend beyond the first main body 118 so as to provide access to the first signal layers 120 and first bridge layers 121. The portions 122 and 123 are also preferably flexible and not bonded to each other proximate distal ends of the portions 122 and 123, so that the portions 122 and 123 are independently moveable. Furthermore, the portions 122 and 123 preferably cooperate to define a first staggered step configuration as shown in Figure 13.

Referring to Figures 15 and 16, the first signal layers 120 are preferably similar to the first signal layers 20 described above with respect to the

first PCB 12. More specifically, each first signal layer 120 preferably includes a first substrate 22, a plurality of first traces 24 and a first ground plane 26 such as described above with respect to each first signal layer 20. Each first trace 24 has a first contact portion 28.

5                   Each first signal layer 120 also preferably includes one or more first alignment features, such as first holes 124. Furthermore, the first holes 124 are preferably formed in the same manner as the first alignment features of the first PCB 12, so as to provide precise registration of the first holes 124 with respect to the first contact portions 28 of a particular first signal layer 120. More specifically, one or  
10                   more first guide features or mask features 125 are preferably formed of the same material and at the same time as the first traces 26 by a photo-etching process. The first mask features 125 cooperate to define a template, and a soft laser is used to ablate first substrate material that is not in the shadow of or otherwise covered by the template so as to define the first holes 124. Alternatively, first substrate material  
15                   may be removed by any suitable ablation process, such as plasma ablation.

Referring to Figures 15 and 17, each first bridge layer 121 includes a first bridge substrate 126, a plurality of first bridge traces 127 disposed on one side of the first bridge substrate 126, and one or more first fill pads 128 disposed on an opposite side of the first bridge substrate 126. The first bridge traces 127 are  
20                   preferably similar to the bridge traces 68, and may be formed in a similar manner. Furthermore, each first bridge trace 127 includes first and second bridge contact portions 129 and 130, respectively. The first bridge contact portions 129 of each first bridge layer 121 are engageable with the contact portions of a particular first ground plane 26.

25                   The first fill pads 128 are preferably formed with similar material and in a similar manner as the first bridge traces 127. Each first bridge layer 121 may also be provided with one or more additional first fill pads 128' disposed on the same side of the first bridge substrate as the first bridge traces 127, but spaced away from the first bridge traces 127. The first fill pads 128 and 128' function to concentrate

clamping forces on the bridge contact portions 129 and 130, as explained below in greater detail.

Each first bridge layer 121 also preferably includes one or more first bridge layer alignment features, such as first bridge layer holes 131. The first bridge layer holes 131 are preferably formed in the same manner as the first alignment features of the first PCB 12, so as to provide precise registration of the first bridge layer holes 131 with respect to the bridge contact portions 129 and 130. More specifically, one or more first bridge layer guide features or mask features 132 are preferably formed of the same material and at the same time as the first bridge traces 127 by a photo-etching process. The first bridge layer mask features 132 cooperate to define a template, and a soft laser is used to ablate first bridge substrate material that is not in the shadow of or otherwise covered by the template so as to define the first bridge layer holes 131. Alternatively, first bridge substrate material may be removed by any suitable ablation process, such as plasma ablation. Furthermore, the first bridge layer holes 131 are alignable with the first holes 124 so as to define the first staggered step configuration.

Referring to Figures 12, 14 and 15, the second PCB 114 has a second main body 133 that includes multiple second signal layers 134 that alternate with second bridge layers 136. A portion 138 of each second signal layer 134 and a portion 139 of each second bridge layer 136 preferably extend beyond the second main body 133 so as to provide access to the second signal layers 134 and second bridge layers 136. The portions 138 and 139 are also preferably flexible and not bonded to each other proximate distal ends of the portions 138 and 139, so that the portions 138 and 139 are independently moveable. Furthermore, the portions 138 and 139 preferably cooperate to define a second staggered step configuration that mates with the first staggered step configuration of the first PCB 112.

Referring to Figures 15 and 18, the second signal layers 134 are similar to the second signal layers 34 described above with respect to the second PCB 14. More specifically, each second signal layer 134 preferably includes a second substrate 36, second traces 38 and a second ground plane 40 such as described above

with respect to each second signal layer 34. Each second trace 38 also has a second contact portion 42. Each second ground plane 40 has contact portions that are engageable with the second bridge contact portions 130 of a particular first bridge layer 121.

5                    Each second signal layer 134 also preferably includes one or more second alignment features, such as second holes 140. Furthermore, the second holes 140 are preferably formed in the same manner as the first holes 124 of the first PCB 112, so as to provide precise registration of the second holes 140 with respect to the second contact portions 42 of a particular second signal layer 134.

10                   Referring to Figures 15 and 19, each second bridge layer 136 includes a second bridge substrate 142, a plurality of second bridge traces 144 disposed on one side of the second bridge substrate 142, and one or more second fill pads 146 disposed on an opposite side of the second bridge substrate 142. The second bridge traces 144 are preferably similar to the bridge traces 68, and may be formed in a  
15                   similar manner. Furthermore, each second bridge trace 144 includes first and second bridge contact portions 148 and 150, respectively. The first bridge contact portions 148 of each second bridge layer 136 are engageable with the first contact portions 28 of a particular first signal layer 120. The second bridge contact portions 150 of each second bridge layer 136 are engageable with the second contact portions 42 of a  
20                   particular second signal layer 134.

                     The second fill pads 146 are preferably formed with similar material and in a similar manner as the second bridge traces 144. Each second bridge layer 136 may also be provided with one or more additional second fill pads 146' disposed on the same side of the second bridge substrate as the second bridge traces 144, but  
25                   spaced away from the second bridge traces 144. The second fill pads 146 and 146' function to concentrate clamping forces on the bridge contact portions 148 and 150, as explained below in greater detail.

                     Each second bridge layer 136 also preferably includes one or more second bridge layer alignment features, such as second bridge layer holes 151. The

second bridge layer holes 151 are preferably formed in the same manner as the first bridge layer holes 131 of the first PCB 112, so as to provide precise registration of the second bridge layer holes 151 with respect to the bridge contact portions 148 and 150. Furthermore, the second bridge layer holes 151 are alignable with the second holes 140 so as to define the second staggered step configuration.

The PCB's 112 and 114 are preferably made in a similar manner as described above with respect to the PCB's 12 and 14. More specifically, each signal layer 120 and 134 and each bridge layer 121 and 136 is preferably made by a photo-etching process followed by laser ablation, or other suitable ablation process, such as described above in detail. The first signal layers 120 and the first bridge layers 121 are then bonded together and cut to a desired size and shape so as to form the first PCB 112 having the first main body 118 and portions 122 and 123 that extend from the first main body 118. Similarly, the second signal layers 134 and the second bridge layers 136 are then bonded together and cut to a desired size and shape so as to form the second PCB 114 having the second main body 133 and portions 138 and 139 that extend from the second main body 133. Alternatively, the signal layers 120 and/or 134 and the bridge layers 121 and/or 136 may be cut or otherwise shaped prior to bonding the signal layers 120 and/or 134 and the bridge layers 121 and/or 136 together.

Referring to Figures 12-14, the connector assembly 116 is used to align and force together the first and second staggered step configurations of the first and second PCB's 112 and 114, respectively, so as to electrically join together the first and second traces 24 and 38, as well as the first and second ground planes 26 and 40. The connector assembly 116 includes first and second fixtures 152 and 154, respectively, and one or more clamping devices such as fasteners 156. The first fixture 152 is connected to the first PCB 112 such that the portions 122 and 123 of the first signal layers 120 and the first bridge layers 121, respectively, cooperate to define the first staggered step configuration. For example, the first fixture 152 may include one or more first alignment members, such as first pins 158, that extend through the first holes 124 of the first signal layers 120 and the first bridge layer holes 131 of the first bridge layers 121. Preferably, the first fixture 152 includes two

first pins 158, each first signal layer 120 includes two first holes 124, and each first bridge layer 121 includes two first bridge layer holes 131. Advantageously, because the portions 122 and 123 are flexible and moveable with respect to each other, the portions 122 and 123 may be easily positioned so as to define the first staggered step configuration.

The second fixture 154 is connected to the second PCB 114 such that the portions 138 and 139 of the second signal layers 134 and the second bridge layers 136, respectively, cooperate to define the second staggered step configuration. For example, the second fixture 154 may include one or more second alignment members such as second pins 160 that extend through the second holes 140 and the second bridge layer holes 151. Preferably, the second fixture 154 includes two second pins 160, each second signal layer 134 includes two second holes 140, and each second bridge layer 136 includes two second bridge layer holes 151. Advantageously, because the portions 138 and 139 are flexible and moveable with respect to each other, the portions 138 and 139 may be easily positioned so as to define the second staggered step configuration.

To electrically join the PCB's 112 and 114, the fixtures 152 and 154 may first be snapped together or otherwise moved toward each other. Preferably, each fixture 152 and 154 includes a flange portion 162 that engages the other fixture 152 or 154 so as to properly align the fixtures 152 and 154 when the fixtures 152 and 154 are moved together. The fasteners 156 are then inserted into corresponding apertures in the fixtures 152 and 154, and the fasteners 156 are tightened so as to apply a clamping force on the portions 122, 123, 138 and 139 of the PCB's 112 and 114. Referring to Figure 15, when the clamping force is applied, the first bridge traces 127 of the first bridge layers 121 electrically join the first and second ground planes 26 and 40, respectively, of the first and second signal layers 120 and 134, respectively. Furthermore, the second bridge traces 144 of the second bridge layers 136 electrically join the first and second traces 24 and 38, respectively, of the first and second signal layers 120 and 134, respectively.



Advantageously, the fill pads 128, 128', 146 and 146' concentrate the clamping force at the contact portions 28, 42, 129, 130, 148 and 150 so as to improve contact between the PCB's 112 and 114. Substrate material may also be removed from the signal layers 120 and 134 and/or the bridge layers 121 and 136, such as described above with respect to the network 10, so as to improve flexibility of one or more of the contact portions 28, 42, 129, 130, 148 and 150.

It is to be understood that the first and second bridge layers 121 and 136, respectively, may also function as signal layers within the first and second main bodies 118 and 133, respectively, of the first and second PCB's 112 and 114, respectively. For example, each bridge layer 121 and 136 may be provided with multiple conductive traces and/or a ground plane.

Figures 20-22 show a third embodiment 210 of the conductive network that includes first and second conductive devices, such as first and second printed circuit boards (PCB's) 212 and 214, respectively, and a connector assembly 216 for connecting together the PCB's 212 and 214. Referring to Figures 23-25, the first PCB 212 includes a first substrate 218, a plurality of first traces 220 disposed on one side of the first substrate 218, and a first ground plane 222 disposed on an opposite side of the first substrate 218. Each first trace 220 includes a generally planar first surface 224 spaced away from the first substrate 218. Each first surface 224 defines a plurality of first edges such as side edges 226 and end edge 228. Preferably, but not necessarily, each end edge 228 is slanted as shown in Figure 23.

First substrate material may also be removed between adjacent first traces 220, in a similar manner as described above, so as to form a plurality of first apertures such as first gaps or notches 229. With such a configuration, each first trace 220 may be independently displaced with respect to the other first traces 220. Alternatively, first substrate material may be removed between select first traces 220, or the first substrate 218 may be left intact.

The first ground plane 222 includes a main portion 230 and a plurality of ground extensions 232 extending from the main portion 230. The main portion

230 may have any suitable configuration such as a solid plane, as shown in Figure 24, or cross-hatched configuration as is known in the art.

5 The first PCB 212 also includes one or more first alignment features such as tabs 234. The tabs 234 are used to align the first traces 224 with respect to the second PCB 214, as explained below in greater detail. The tabs 234 also provide support for the ground extensions 232.

10 The tabs 234 are preferably formed in the same manner as the first alignment features of the first PCB 12, so as to provide precise registration of the tabs 234 with respect to the first traces 224. More specifically, each tab 234 preferably has a backing layer 236 that is formed in the same manner and at the same time as each first trace 224. The backing layers 236 are preferably used as guide features or mask features for forming the tabs 234 by laser ablation, or other suitable ablation process, such as described above in detail. Advantageously, the backing layers 236 also increase stiffness of the tabs 234.

15 The second PCB 214 includes a second substrate 238, a plurality of second traces 240 disposed on one side of the second substrate 238, and a second ground plane 242 disposed on an opposite side of the second substrate 238. Each second trace 240 includes a generally planar second surface 244 spaced away from the second substrate 238. Each second surface 244 defines a plurality of second edges such as side edges 246.

20 The second PCB 214 also includes one or more second alignment features such as slots 248. The slots 248 are configured to receive the tabs 234 so as to align the traces 220 and 240 in a desired orientation. Preferably, as shown in Figure 25, the traces 220 and 240 are aligned such that the first traces 220 are laterally offset with respect to the second traces 240.

The slots 248 are preferably formed in the same manner as the second alignment features of the second PCB 14, so as to provide precise registration of the slots 248 with respect to the second traces 240. More specifically, U-shaped guide

features or mask features 250 are preferably formed in the same manner and at the same time as the second traces 240. The mask features 250 are then used as a cutting template for forming the slots 248 by laser ablation, or other suitable ablation process, such as described above in detail.

5                   The PCB's 212 and 214 are preferably made in a similar manner as described above with respect to the PCB's 12 and 14. More specifically, each PCB 212 and 214 is preferably made by a photo-etching process followed by laser ablation, or other suitable ablation process, such as described above in detail.

10                   Referring to Figures 20-22, the connector assembly 216 is used to electrically join together the first and second traces 220 and 240, respectively, as well as the first and second ground planes 222 and 242, respectively. The connector assembly 216 includes first and second connector portions or fixtures 252 and 254, respectively, and a clamping device 256. The first and second fixtures 252 and 254 are engageable with each other so as to join the PCB's 212 and 214 at an angle with  
15                   respect to each other, as shown in Figure 22.

                  The first fixture 252 is connected to the first PCB 212 in any suitable manner, and includes a cavity 258 for receiving the second fixture 254. The first fixture 252 further includes one or more guide slots 260, and each guide slot 260 preferably includes an enlarged portion 261. The first fixture 252 also includes the  
20                   clamping device 256, which is preferably a flexible latch as shown in Figure 20.

                  Alternatively, the clamping device 256 may be provided as part of the second fixture 254, or separate from either fixture 252 or 254. Furthermore, the clamping device 256 may be any suitable device such as one or more screws or bolts (not shown).

25                   The second fixture 254 is connected to the second PCB 214 in any suitable manner, and includes a projection 262 that is insertable into the cavity 258. The second fixture 254 further preferably includes first and second guide pegs 264

and 266, respectively, on both sides of the second fixture 254 (only one pair of guide pegs 264 and 266 is shown in Figure 21).

5 To electrically join the PCB's 212 and 214, the second fixture 254 is inserted into the first fixture 252 such that the guide pegs 264 and 266 are inserted into the guide slots 260. As the fixtures 252 and 254 are moved toward each other, the tabs 234 mesh with the slots 248 so as to properly align the first traces 220 with the second traces 240. When the first guide pegs 264 reach the enlarged portions 261 of the guide slots 260, the second fixture 254 rotates slightly clockwise, with  
10 respect to Figures 21 and 22. The second fixture 254 is then clamped against the first fixture 252 with the clamping device 256.

Referring to Figure 22, because the second fixture 254 is able to rotate slightly with respect to the first fixture 252, the angle  $\alpha$  between the PCB's 212 and 214 increases slightly. This action produces a clamping or contact force which  
15 forces the traces 220 and 240 against each other. More specifically, referring to Figures 23- 25, the end edge 228 of each first trace 220 is forced against a side edge 246 of a particular second trace 240 so as to define or otherwise form an area of contact, which in this case is a point contact. Because the traces 220 and 240 are forced together at an angle, a portion of each first surface 224 proximate a respective  
20 point contact is non-parallel with a portion of a respective second surface 244 proximate the respective point contact.

Advantageously, because each pair of mating edges 228 and 246 preferably forms a point contact, the contact force may be concentrated over a relatively small area. As a result, the traces 220 and 240 may be sufficiently  
25 deformed at the point contacts so as to establish reliable connections between the traces 220 and 240.

Furthermore, because the edges 228 and 246 are precisely defined by the photo-etching process, the edges 228 and 246 are sharp. Preferably the radius of each edge 228 and 246 is less than 0.1mm. Thus, when the edges 228 and 246  
30 are forced together, the edges 228 and 246 are able to break through oxides and

displace any insulating debris that may have collected on the traces 220 and 240.

Because the first traces 220 are preferably independently moveable as described above, the conductive network 210 is able to overcome any non-coplanarity of the first traces 220 and/or second traces 240. Alternatively or supplementally, the second PCB 214 may be provided with apertures, such as notches, between adjacent second traces 240, so that each second trace 240 is independently moveable.

It should be noted that if the end edges 228 of the first traces 220 are not slanted, when the end edges 228 are forced against the second traces 240, each end edge 228 will form an area of contact, such as a line of contact, with a particular second surface 244. Again, however, a portion of each first surface 224 proximate a respective line of contact will be non-parallel with a portion of a respective second surface 244 proximate the respective line of contact.

The contact force also forces together the ground extensions 232 and the second ground plane 242. Each ground extension 232 forms an area of contact, such as a line of contact, with an end edge 266 of the second ground plane 242. Alternatively, each ground extension 232 may be provided with a slanted end edge that precisely mates with an end edge 266 of the second ground plane 242 so as to form a point contact.

As mentioned above, the tabs 234 preferably include backing layers 236 to increase stiffness of the tabs 234, which thereby increases the contact force. Similarly, the second ground plane 242 may be extended between the slots 248 to increase the stiffness of the second PCB 214 and thereby increase the contact force.

It should be noted that there are various other alternatives for joining an edge of one trace to a portion of another trace so as to form an area of contact such as a line of contact or a point contact. For example, Figure 26 shows a fourth embodiment 310 of the conductive network according to the invention that includes first and second conductive devices, such as first and second PCB's 312 and 314,

respectively. The first PCB 312 includes a first substrate 316, a plurality of first traces 318 disposed on one side of the first substrate 316, and a first ground plane 320 disposed on an opposite side of the first substrate 316. Each first trace 318 includes a generally planar first surface 322 spaced away from the first substrate 316.

5 Each first surface 322 defines a plurality of first edges such as first side edges 324 and first end edges 326. Similar to the first PCB 212 described above, each first end edge 326 is preferably slanted.

The second PCB 314 includes a second substrate 328, a plurality of second traces 330 disposed on one side of the second substrate 328, and a second ground plane 332 disposed on an opposite side of the second substrate 328. Each second trace 330 includes a generally planar second surface 334 spaced away from the second substrate 328. Each second surface 334 defines a plurality of second edges such as second side edges 336 and second end edges 338. Preferably, but not necessarily, each second end edge 338 may be formed with a staggered step configuration as shown in Figure 26. For clarity purposes, the second substrate 328 has been broken away in Figure 26 so as to reveal ends of two of the second traces 330.

10

15

Furthermore, substrate material may also be removed between adjacent second traces 330 so as to form a plurality of apertures such as gaps or notches 339. With such a configuration, each second trace 330 may be independently displaced with respect to the other second traces 330. Alternatively, substrate material may be removed between select second traces 330.

20

To electrically join the PCB's 312 and 314, the PCB's 312 and 314 are positioned adjacent each other and are forced together such as with any suitable clamping device. With the configuration of the PCB's 312 and 314 described above, it is not necessary that the PCB's 312 and 314 be joined at an angle. Instead, because of the notches 339, each second trace 330 may twist or otherwise rotate with respect to a particular first trace 318 when the PCB's 312 and 314 are forced together. As a result, each first end edge 326 will mate with a second surface 334 and/or a second end edge 338 of a particular second trace 330. For example, if each

25

30

second trace 330 does not substantially flex vertically as the PCB's 312 and 314 are forced together, then a distal portion 340 of each second end edge 338 may mate with a particular first end edge 326 so as to define or form a point contact. If, however, each second trace 330 does flex vertically as the PCB's 312 and 314 are forced together, then each first end edge 326 may mate with a second surface 334 of a particular second trace 330 so as to form a line of contact. In this case, at least one of the intermediate portions 342 of each second end edge 338 will preferably form a point contact with a respective first end edge 326.

Because the second traces 330 are able to twist with respect to the first traces 318, each second surface 334 proximate a respective end of a respective second trace 330 will be non-parallel with a respective first surface 322 of a respective first trace 318. Thus, no matter what type of area of contact is formed between a pair of mating traces 318 and 330, a portion of each first surface 322 proximate a respective area of contact will preferably be non-parallel with a portion of a respective second surface 334 proximate the respective area of contact.

Alternatively or supplementally, the first PCB 312 may be provided with apertures such as gaps or notches between adjacent first traces 318 so that the each first trace 318 may twist or otherwise rotate. In any case, the first traces 318 are preferably laterally offset with respect to the second traces 330 to facilitate twisting of the traces 318 and/or 330.

It should be noted that with such a configuration as shown in Figure 24, the ground planes 320 and 332 may be connected together in any suitable manner. For example, the ground planes 320 and 332 may be connected together with micro-vias 344. Furthermore, the micro-vias 344 may be formed by laser ablation, or other suitable ablation process, as is known in the art.

As another example, an area of contact between a pair of traces may be formed by angling the traces horizontally and vertically with respect to each other, such that a side edge of one trace will intersect a surface (so as to form a line of contact) or a side edge (so as to form a point contact) of the other trace.

In general terms, an area of contact between an edge of one trace and a surface or edge of another trace may be formed by establishing two angles between the traces to be joined, wherein both angles are preferably less than 180 degrees. For example, as described above, a first angle may be established between two traces by configuring an end edge of one trace so that the end edge is slanted with respect to a side edge of the other trace. Alternatively, the first angle may be established between the traces by configuring the end edge of the one trace so that it is perpendicular with respect to the side edge of the other trace. A second angle may be established between the two traces by tilting the one trace with respect to the other trace so that the end edge of the one trace contacts the side edge or a surface of the other trace. Alternatively, the second angle may be established between the traces by rotating or twisting one trace with respect to the other trace.

While embodiments of the invention have been illustrated and described, it is not intended that these embodiments illustrate and describe all possible forms of the invention. Rather, the words used in the specification are words of description rather than limitation, and it is understood that various changes may be made without departing from the spirit and scope of the invention. For example, although for each embodiment described above, the conductive devices are shown as printed circuit boards, each conductive device may be any suitable transmission line structure provided with or without a ground plane.



**WHAT IS CLAIMED IS:**

- 1                    1.     A method of forming a conductive device, the method  
2 comprising:  
3                    forming a conductive layer on a substrate;  
4                    etching the conductive layer to form a plurality of conductive traces;  
5                    etching the conductive layer to form at least one mask feature; and  
6                    removing substrate material that is not covered by the at least one  
7 mask feature so as to form at least one mechanical alignment feature.
- 1                    2.     The method of claim 1 wherein the etching steps are  
2 performed simultaneously.
- 1                    3.     The method of claim 1 wherein the removing step includes  
2 removing the substrate material with a laser.
- 1                    4.     The method of claim 1 wherein the removing step includes  
2 removing the substrate material so as to form at least one aperture.
- 1                    5.     The method of claim 1 wherein the removing step includes  
2 removing the substrate material so as to form at least one side edge.
- 1                    6.     The method of claim 1 wherein the removing step includes  
2 removing the substrate material so as to form at least one tab.
- 1                    7.     The method of claim 1 wherein the removing step includes  
2 removing the substrate material so as to form at least one slot.
- 3                    8.     A conductive device produced according to the method of  
4 claim 1.
- 1                    9.     A method of forming a printed circuit board, the method  
2 comprising:

3                   forming a conductive layer on a substrate;  
4                   etching the conductive layer to form multiple conductive traces,  
5 each trace having a contact portion,  
6                   etching the conductive layer to form multiple mask features that  
7 cooperate to define a template; and  
8                   ablating with a laser substrate material that is not covered by the  
9 template so as to form a plurality of mechanical alignment features.

1                   10.     The method of claim 9 wherein the etching steps are  
2 performed simultaneously.

1                   11.     The method of claim 9 wherein the ablating step comprises  
2 ablating substrate material so as to form a multiple apertures and multiple side  
3 edges, wherein the apertures and the side edges function as mechanical alignment  
4 features.

1                   12.     A printed circuit board produced according to the method of  
2 claim 9.

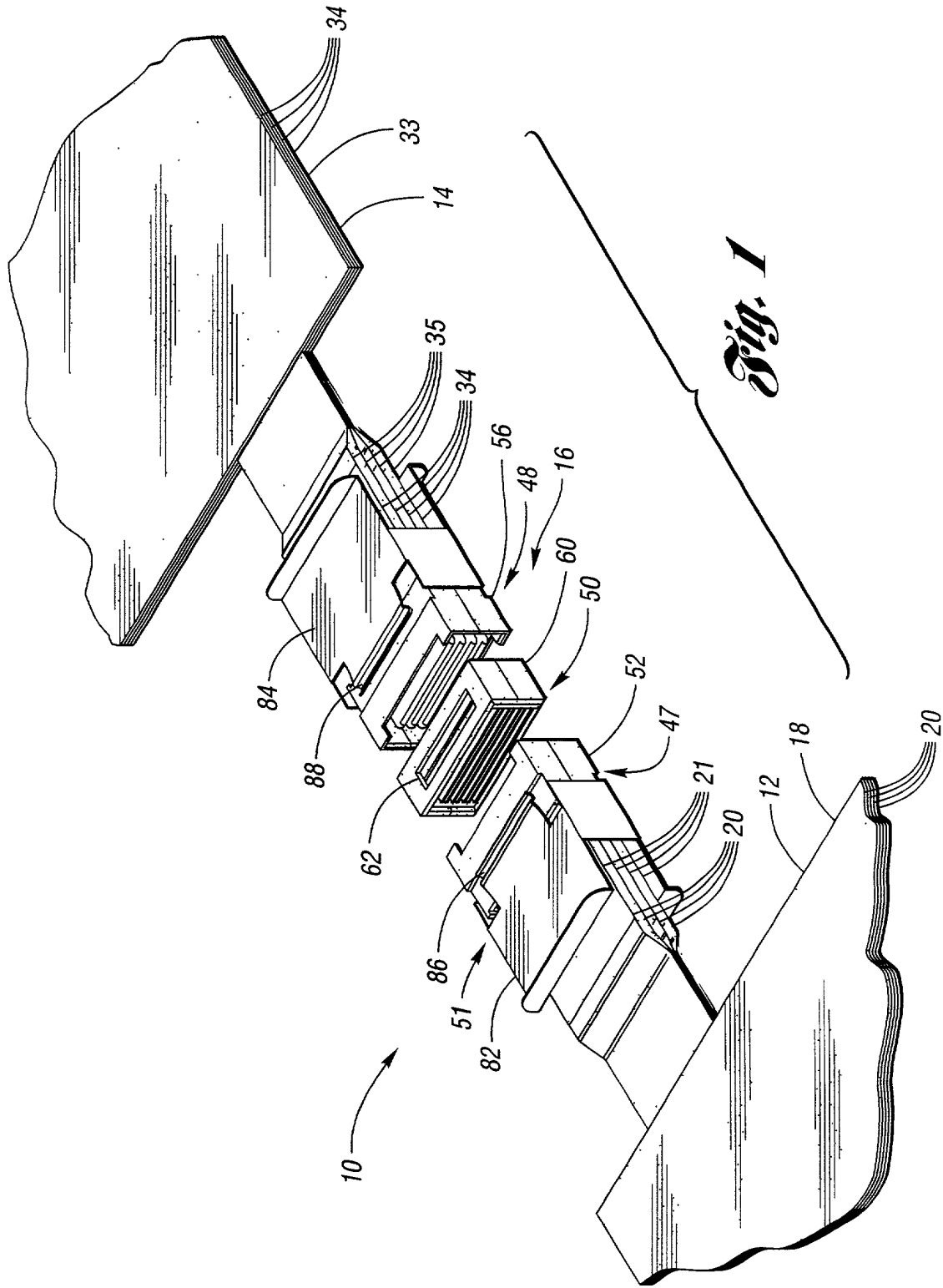


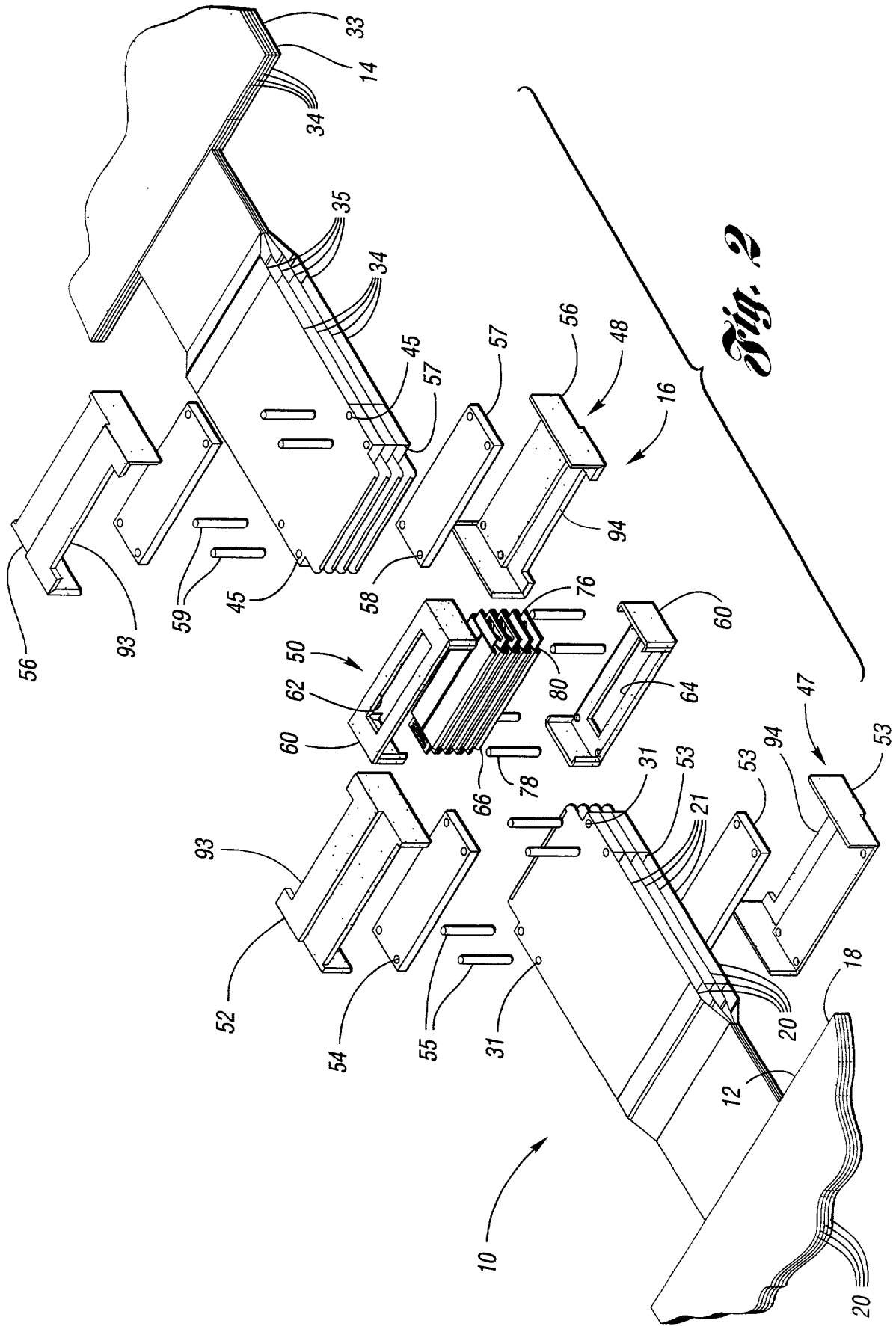
Revised - 08/15/00, Mark

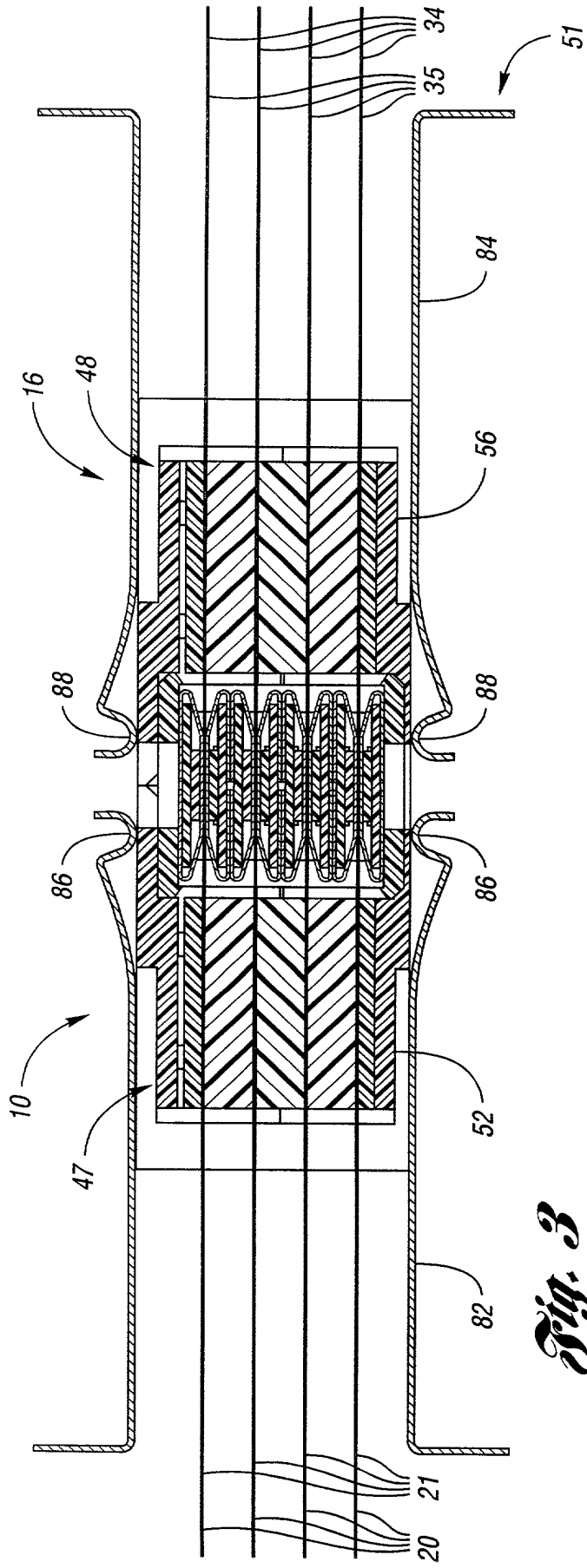
09/10/00, Mark

09/13/00, Shelly

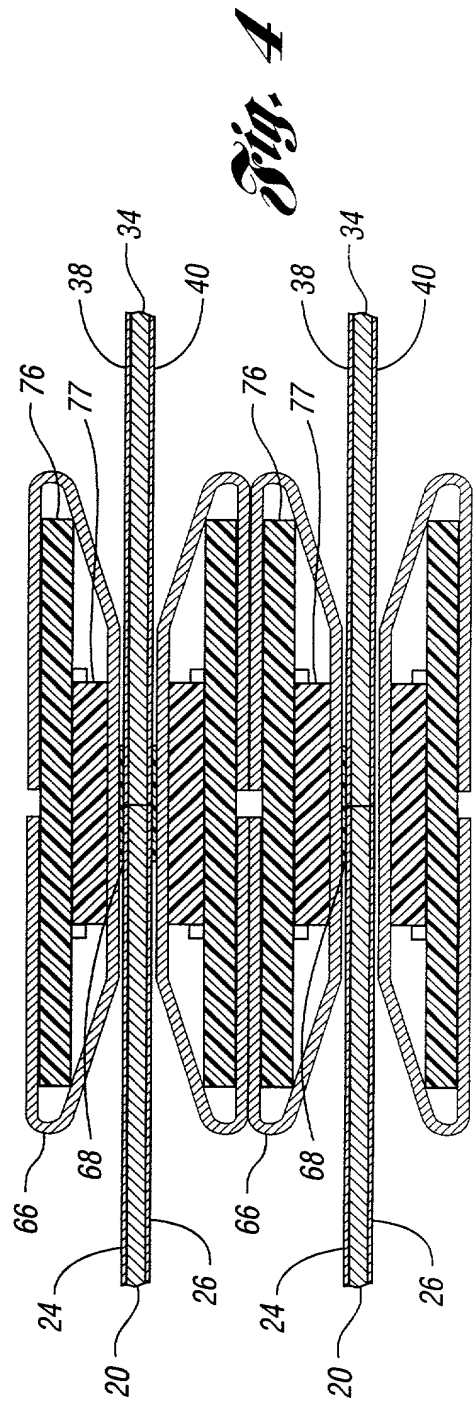
10/05/00, Shelly



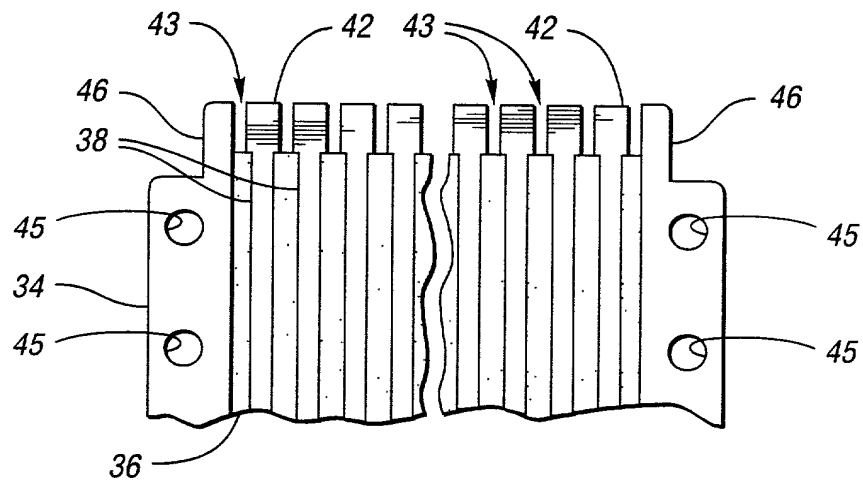
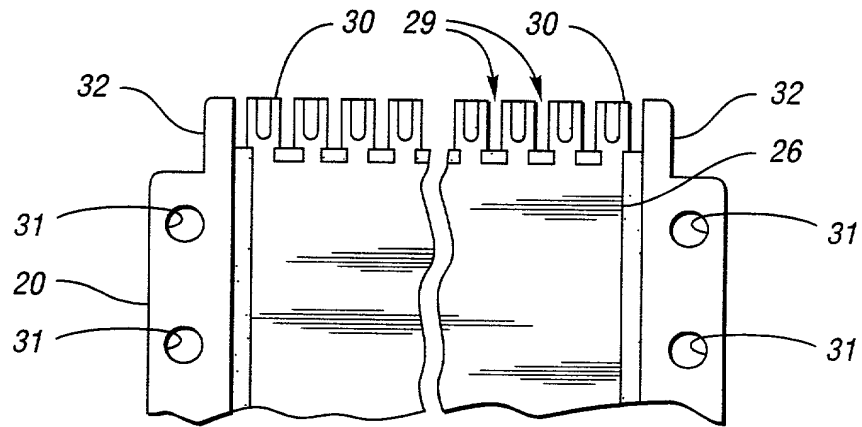
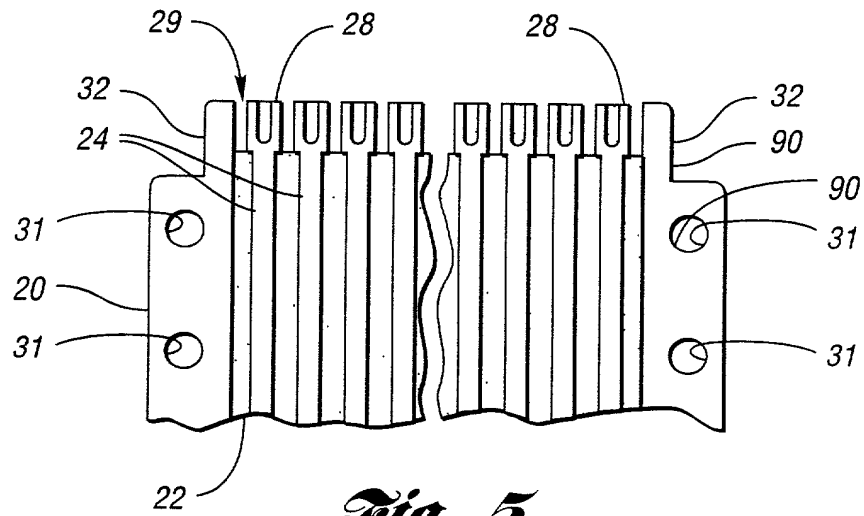




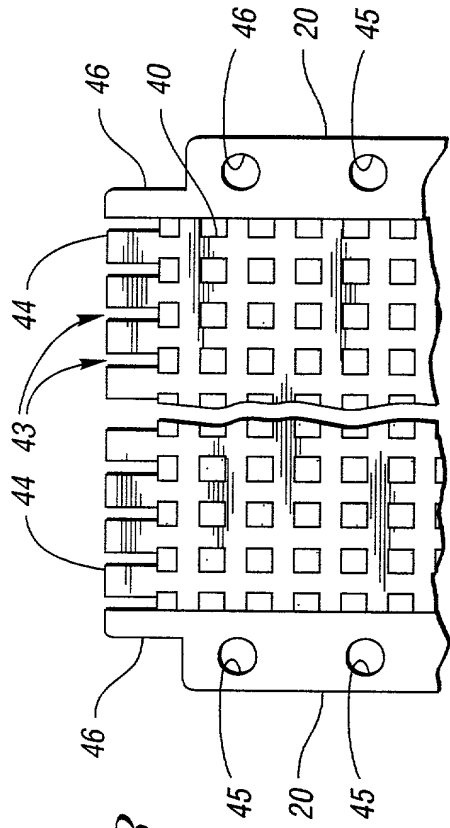
*Fig. 3*



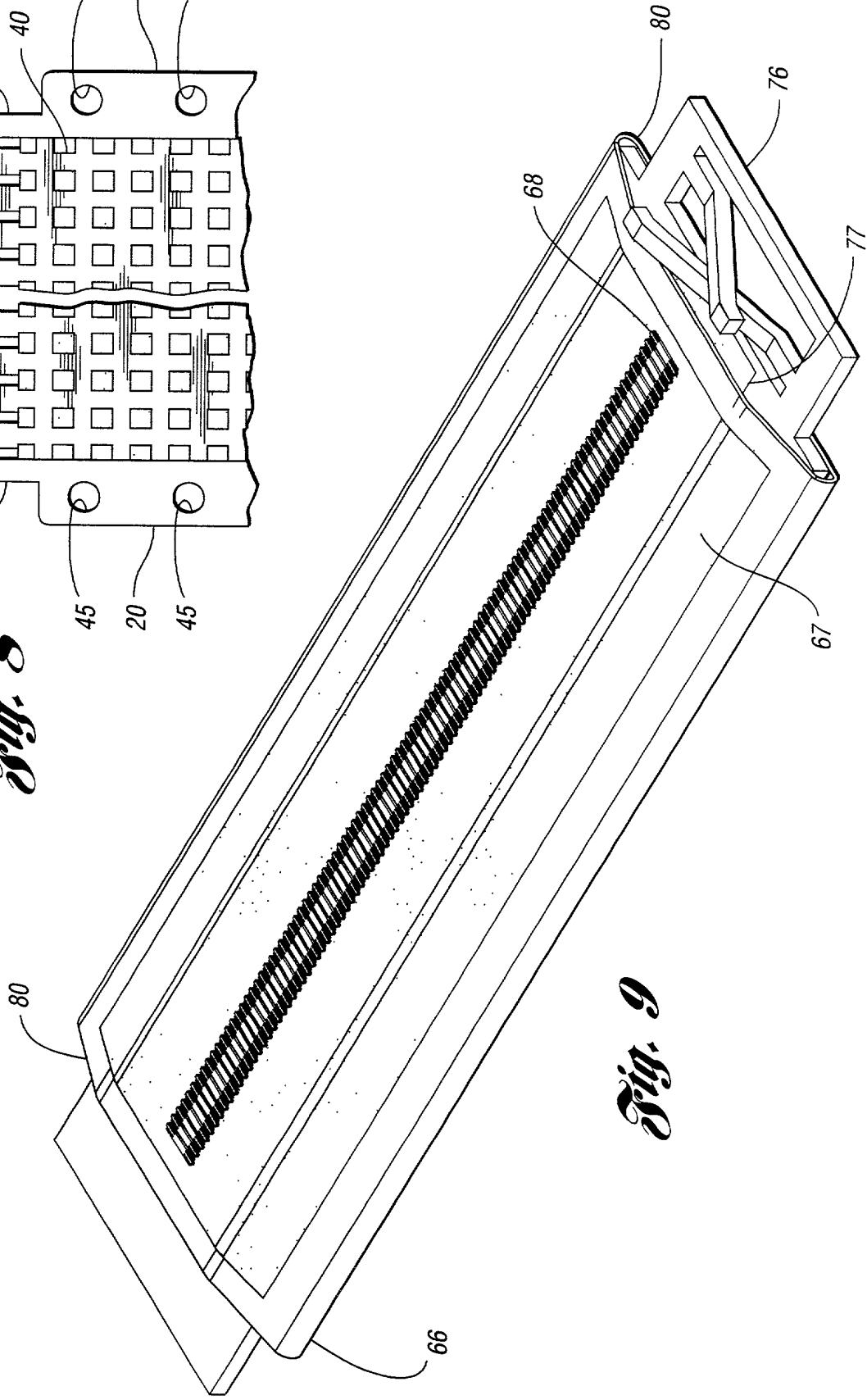
*Fig. 4*



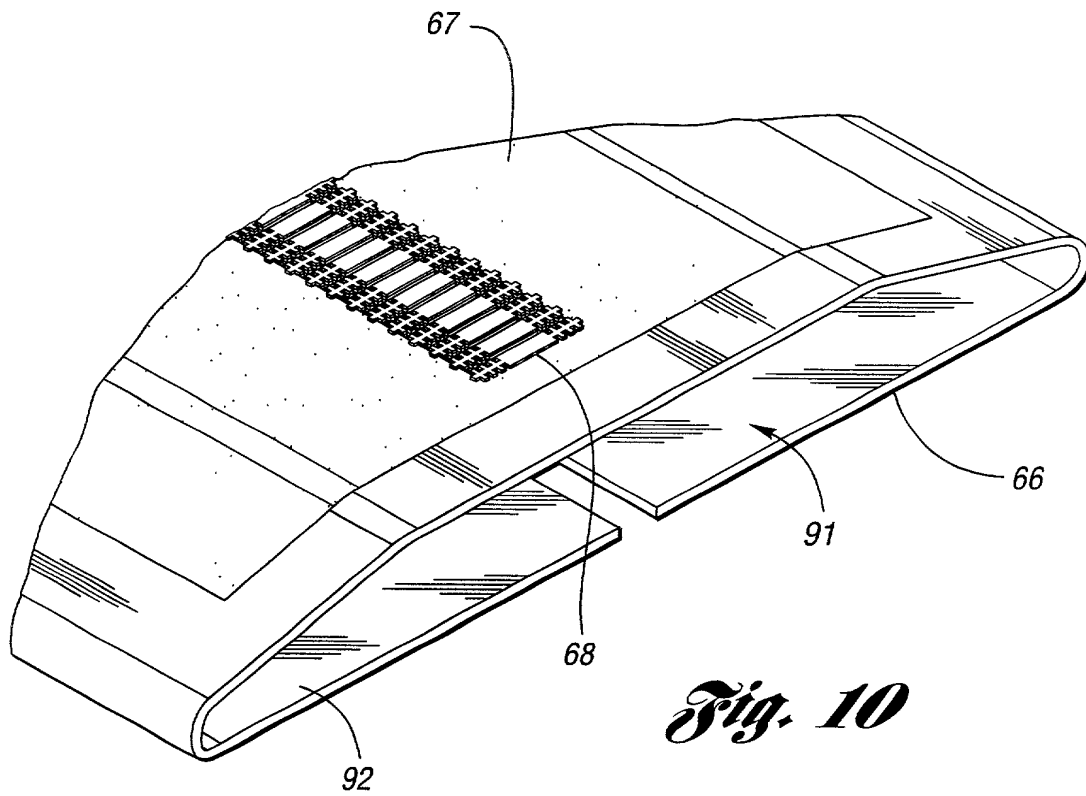




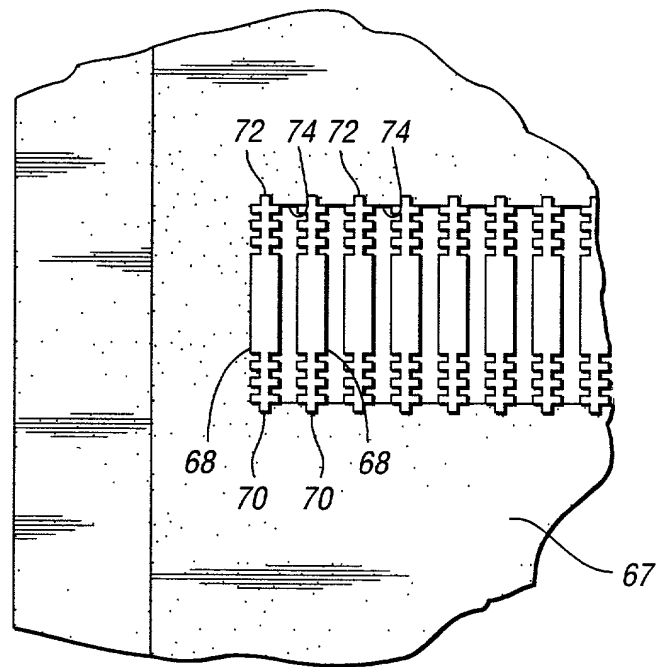
*Fig. 8*



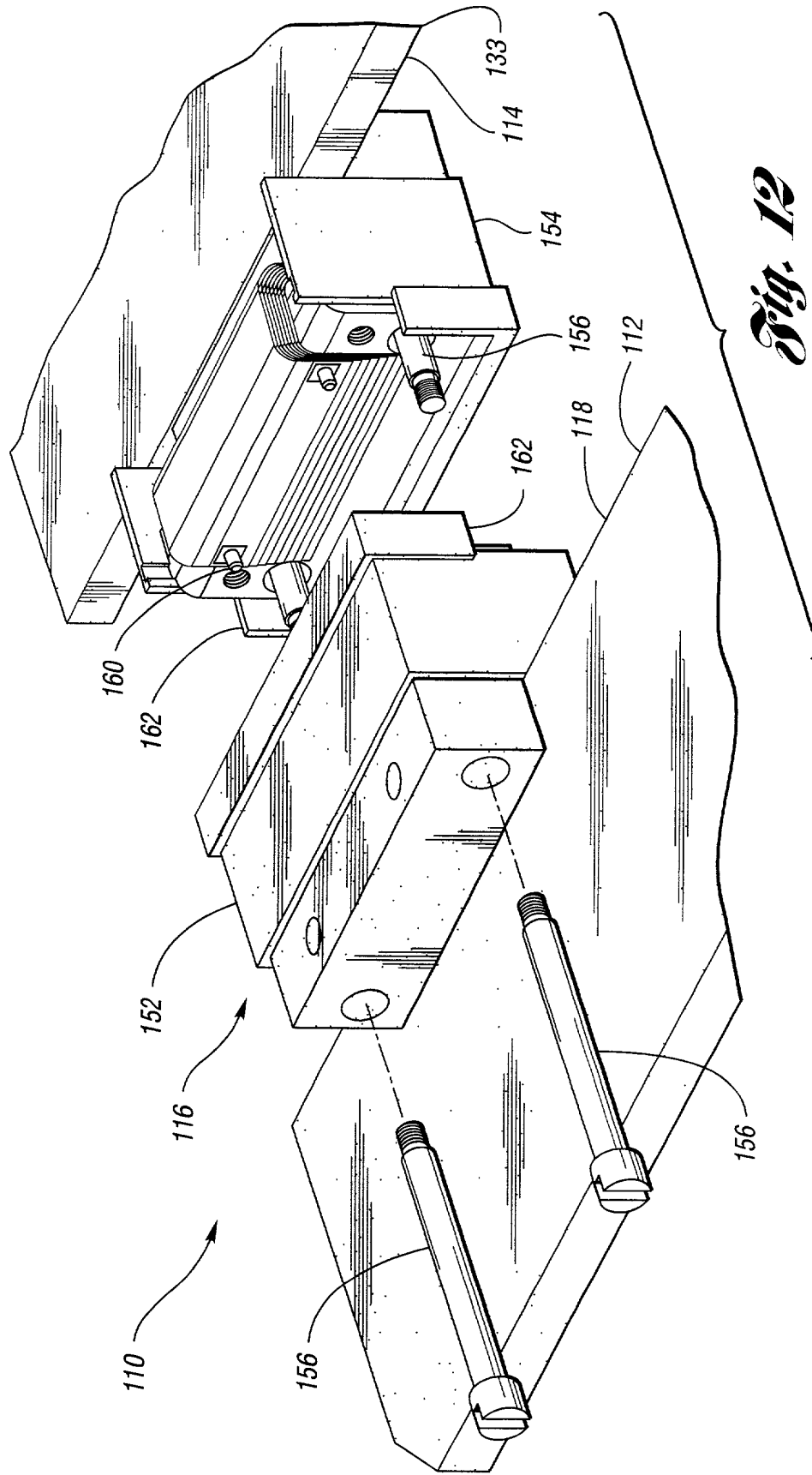
*Fig. 9*

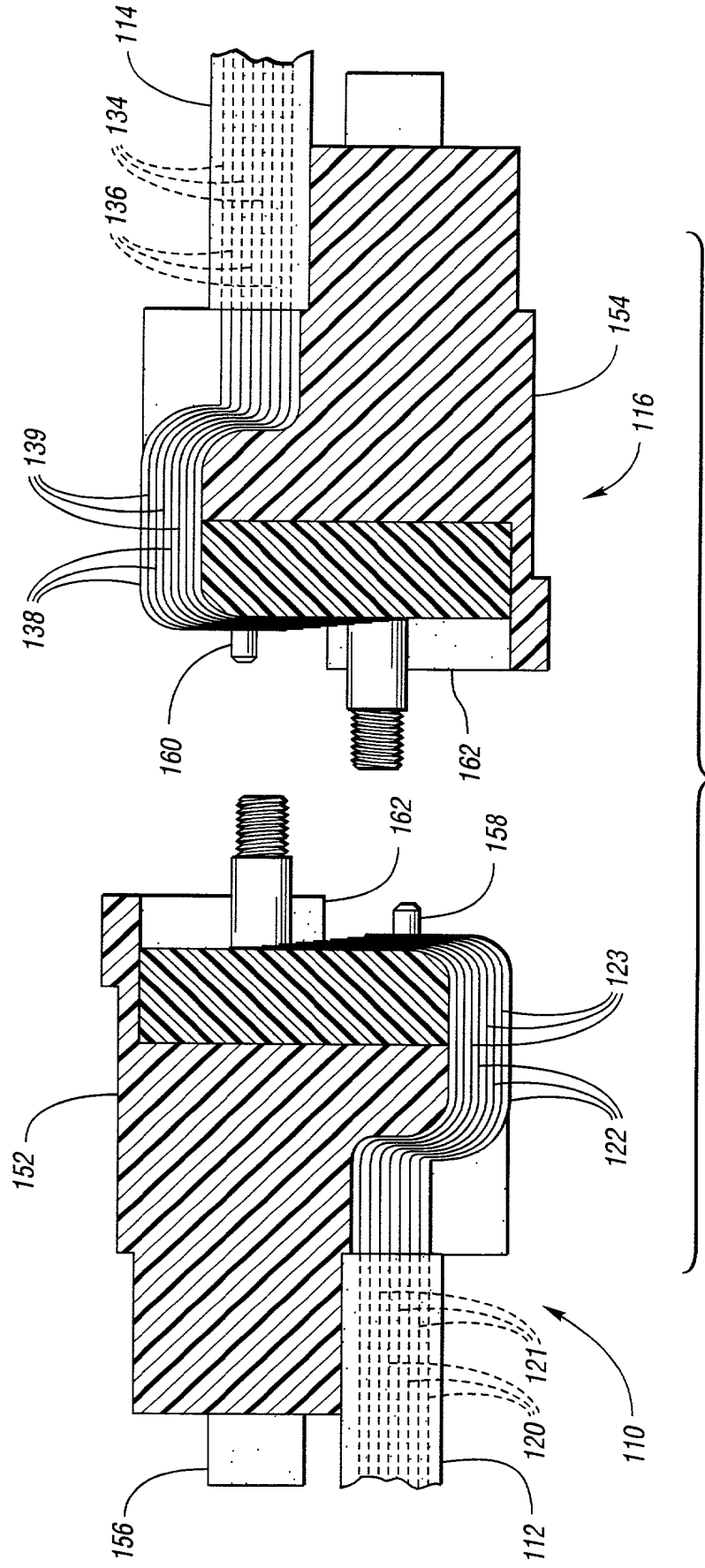


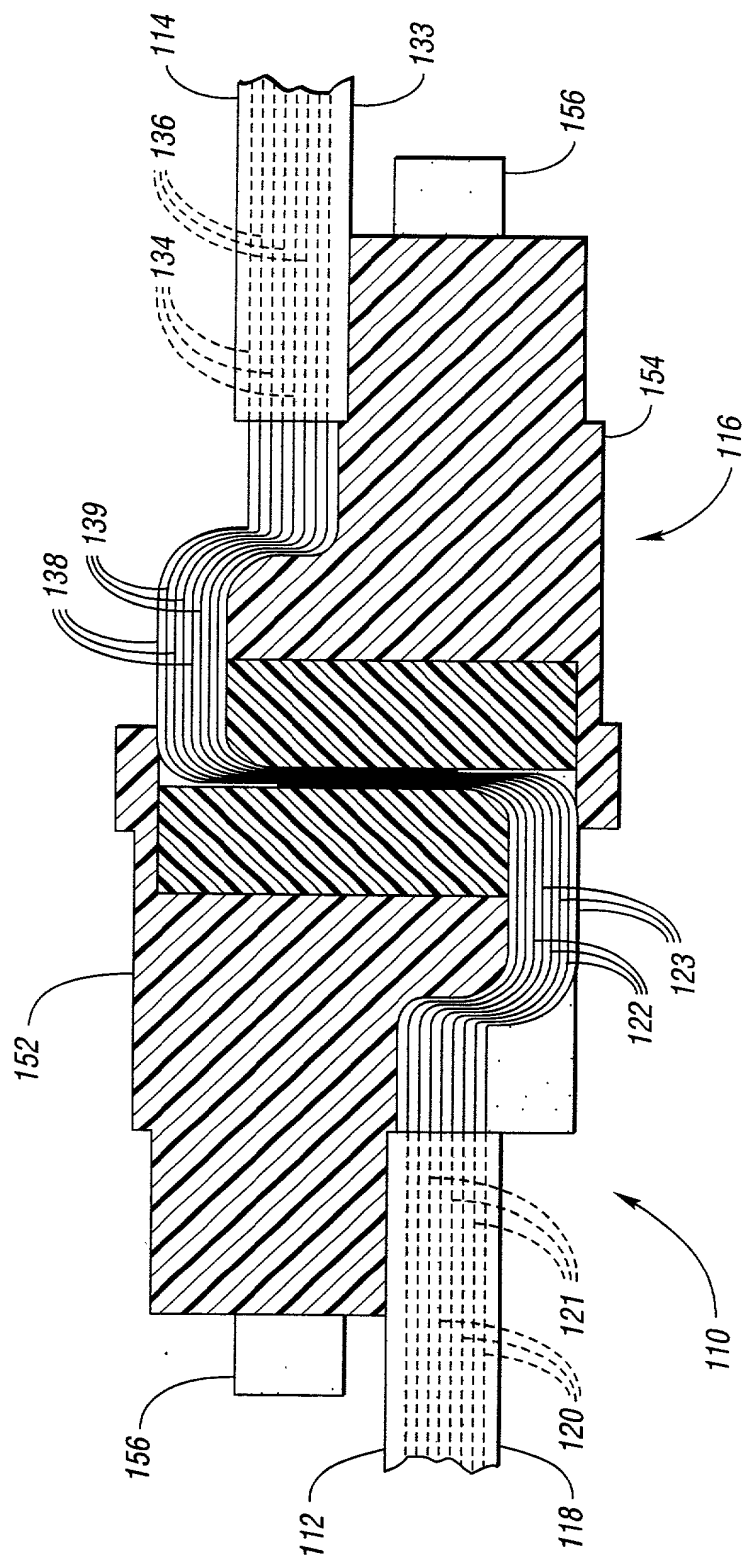
*Fig. 10*



*Fig. 11*

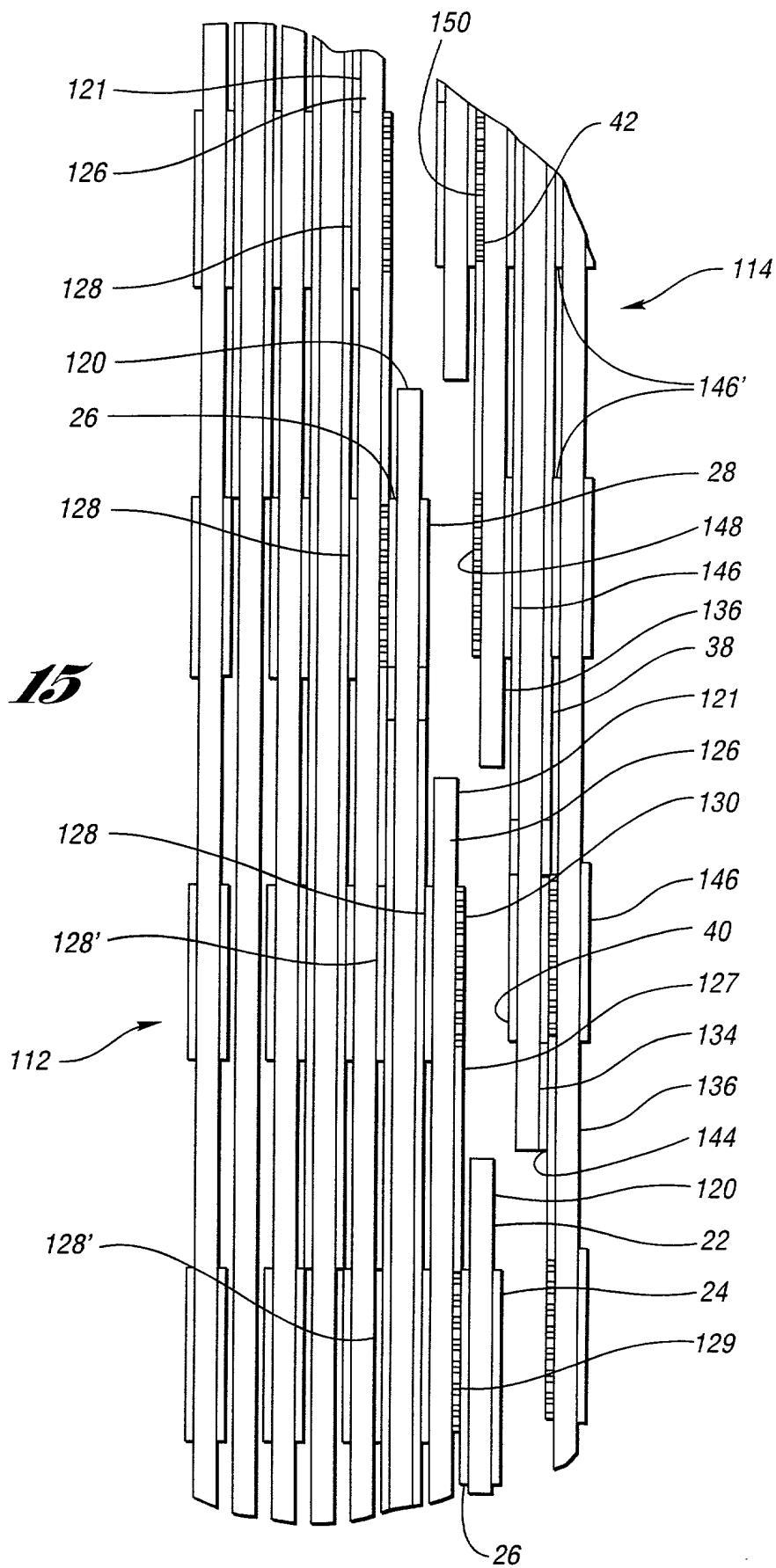


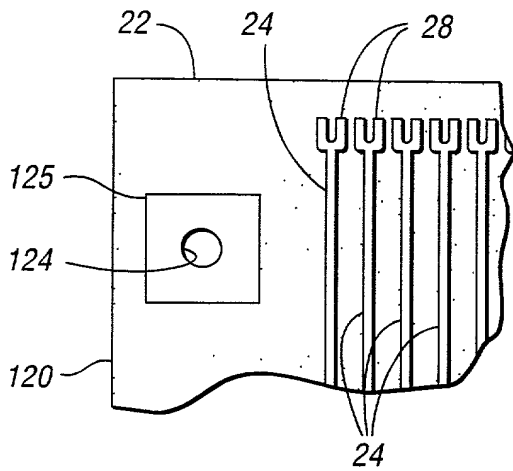




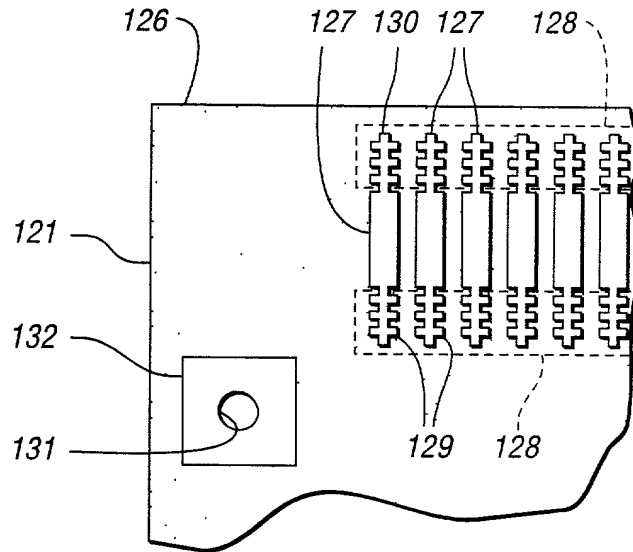
*Fig. 14*

**Fig. 15**

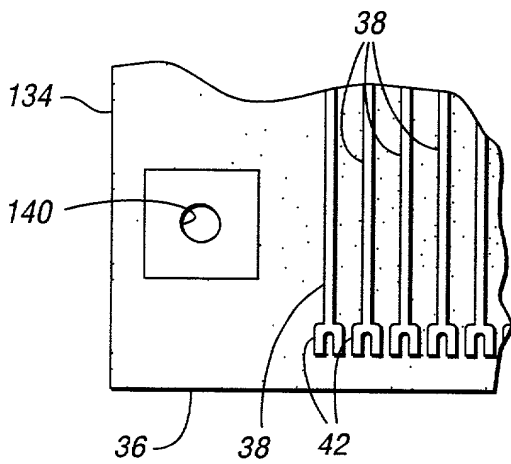




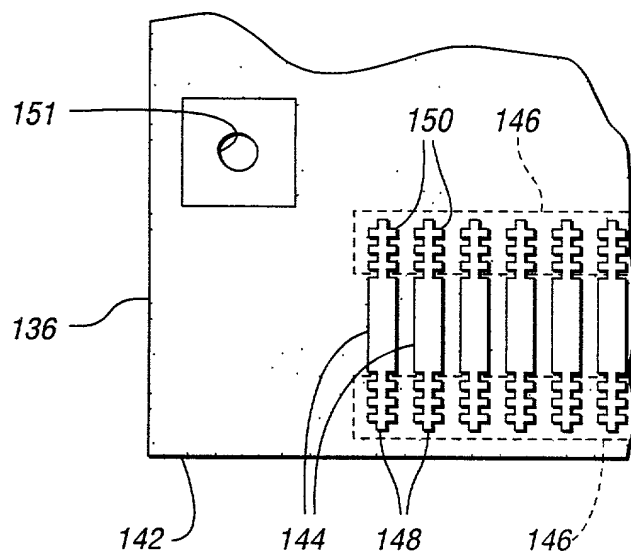
**Fig. 16**



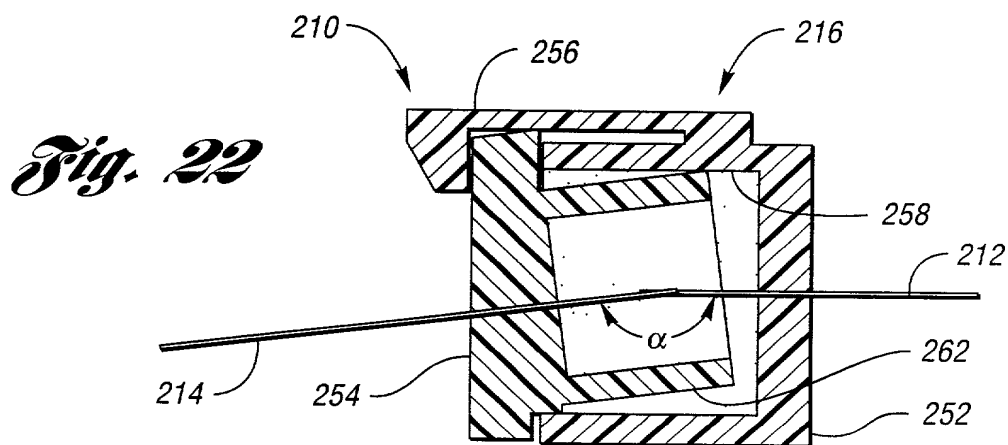
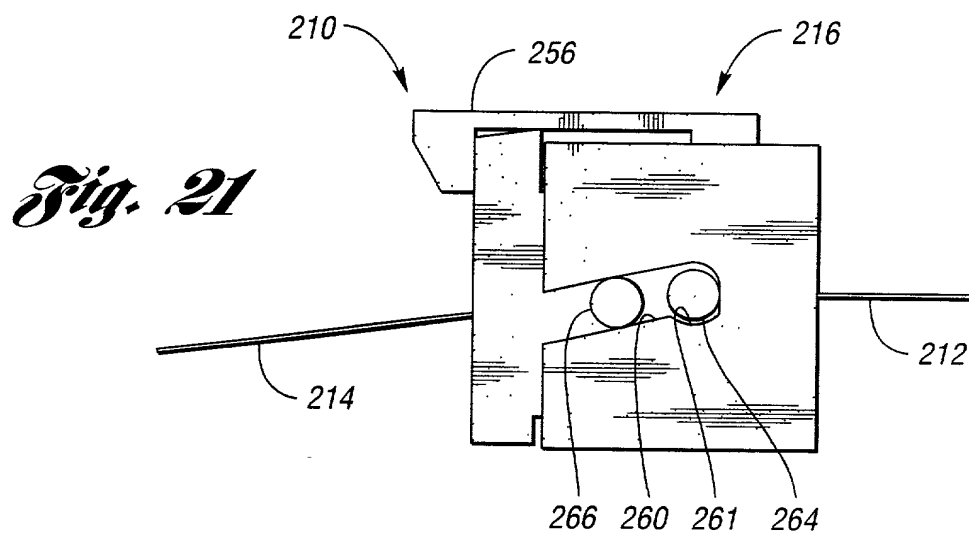
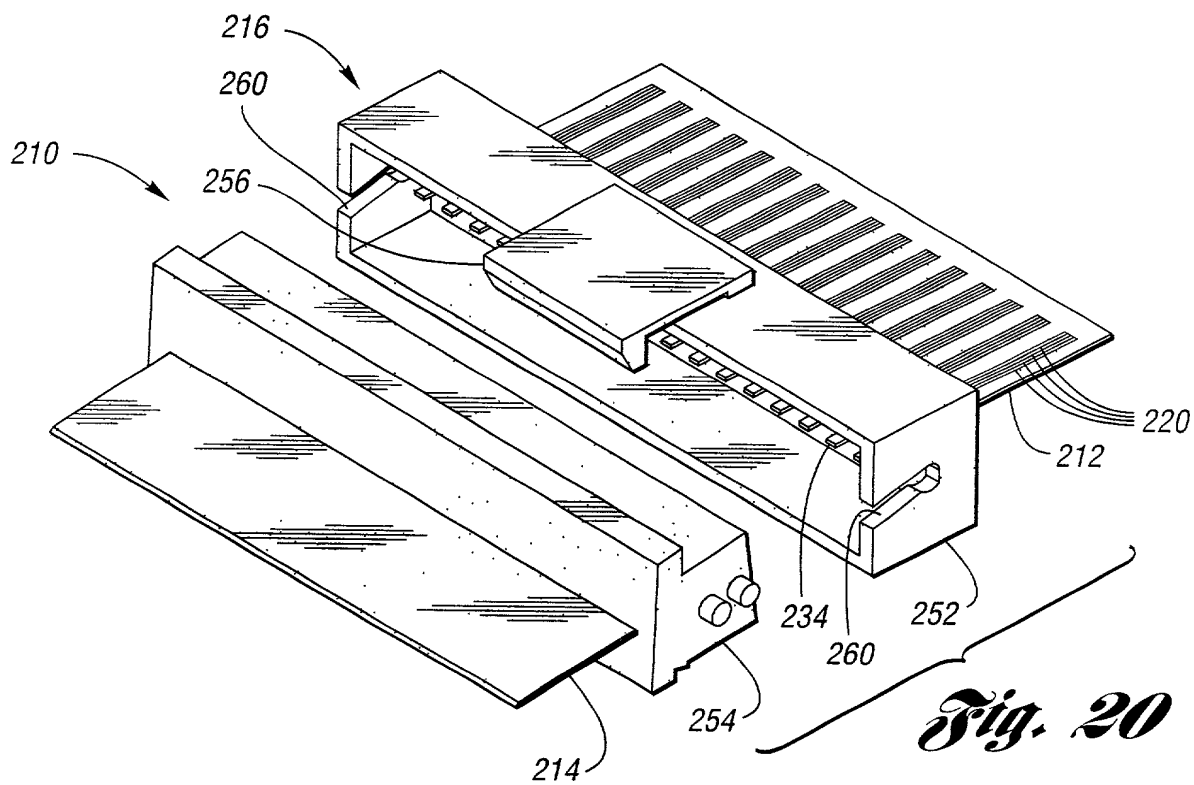
**Fig. 17**



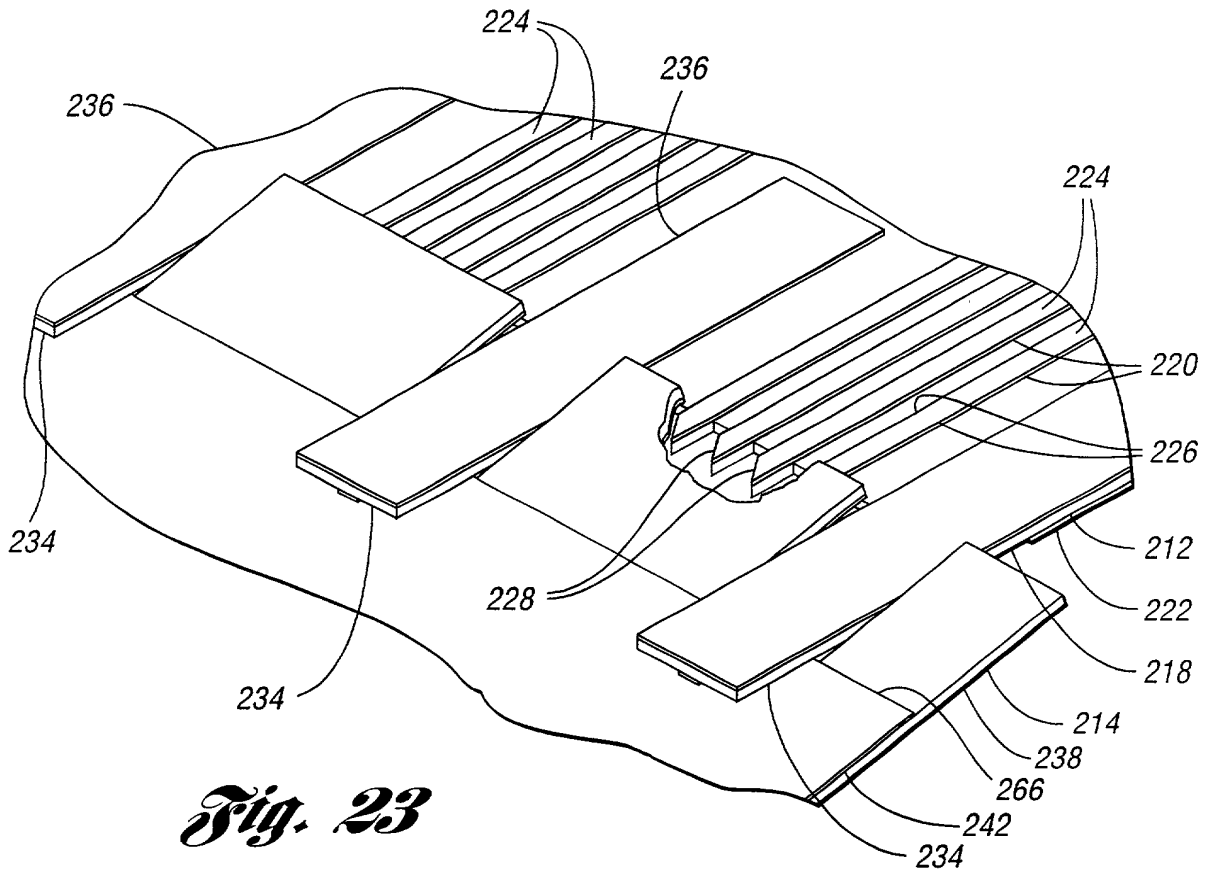
**Fig. 18**



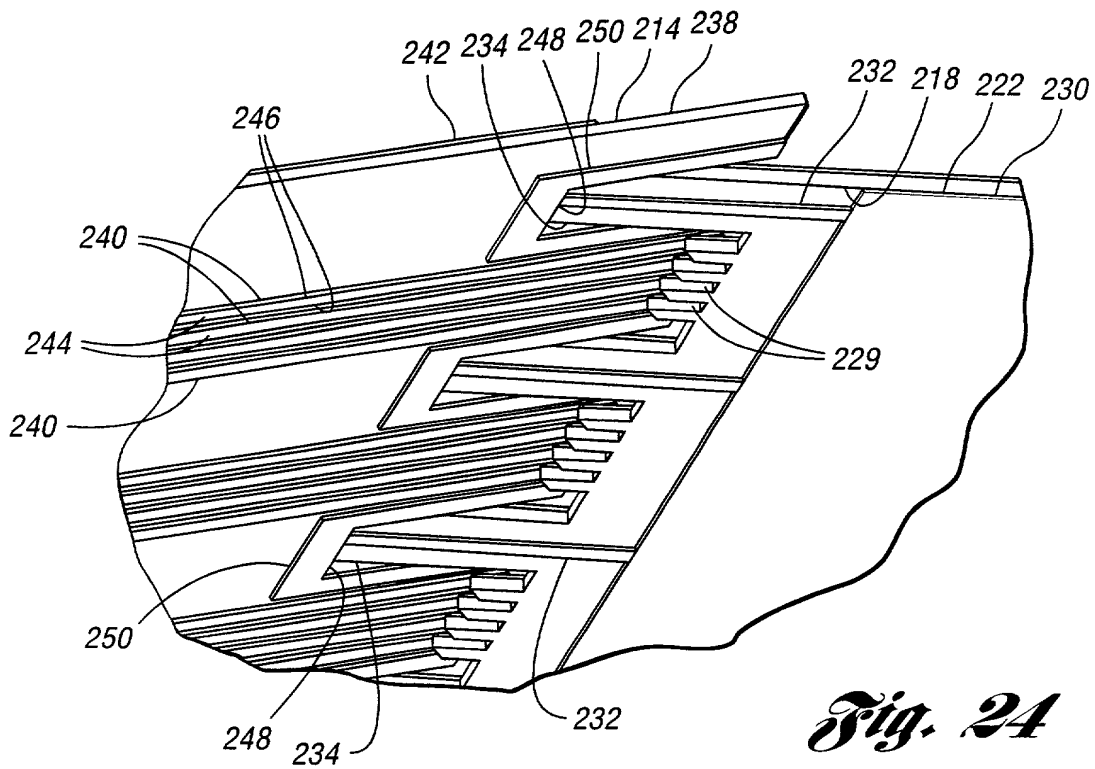
**Fig. 19**



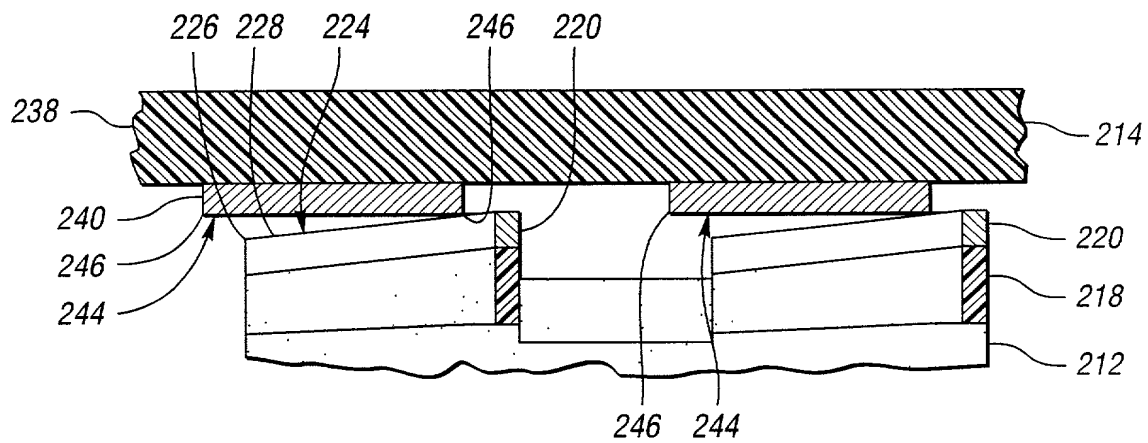




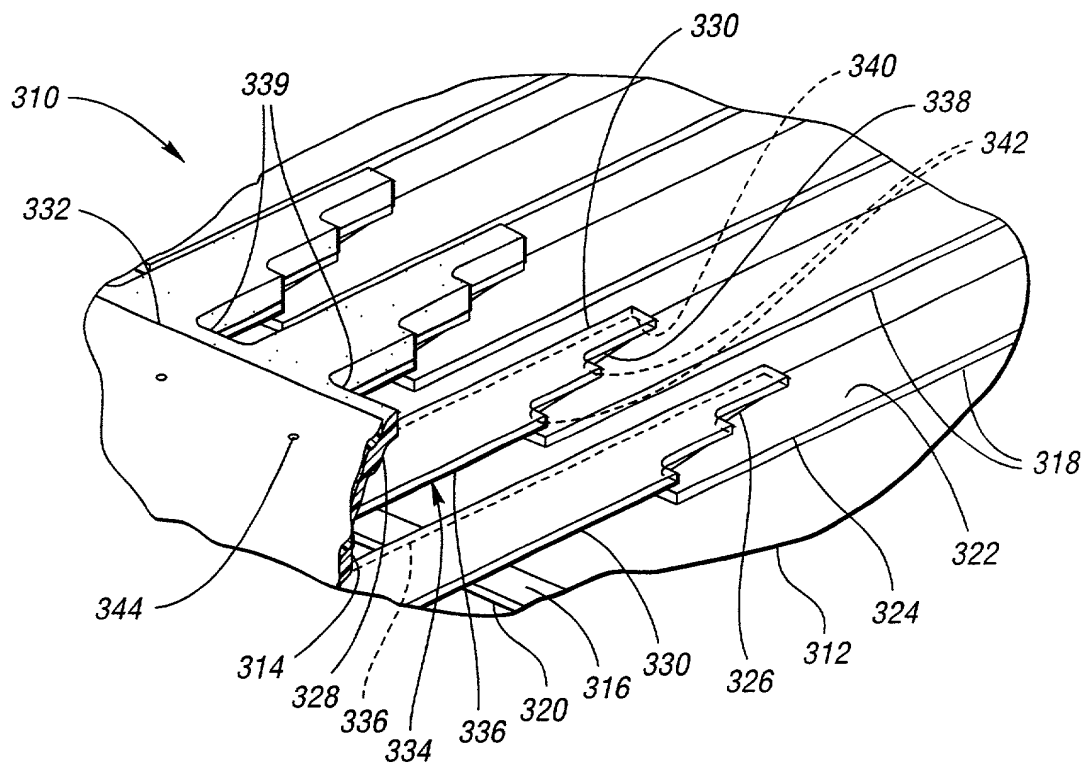
**Fig. 23**



**Fig. 24**



*Fig. 25*



*Fig. 26*

## DECLARATION FOR PATENT APPLICATION AND POWER OF ATTORNEY

Atty. Docket No. 00-046-NSC  
First Named Inventor John W. Svenkeson et al.

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

### METHOD OF FORMING ALIGNMENT FEATURES FOR CONDUCTIVE DEVICES

the specification of which:

☒ is attached hereto; or  
☐ was filed on (MM/DD/YYYY) \_\_\_\_\_ as U.S. Application Number or PCT International Application Number \_\_\_\_\_, and was amended on (MM/DD/YYYY) \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below, and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

<i>Prior Foreign Application Number(s)</i>	<i>Country</i>	<i>Foreign Priority Date (MM/DD/YYYY)</i>	<i>Priority Not Claimed</i>	<i>Certified Copy Attached? (Yes/No)</i>

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below.

<i>Application Number(s)</i>	<i>Filing Date (MM/DD/YYYY)</i>

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

<i>Application Number(s)</i>	<i>Filing Date (MM/DD/YYYY)</i>	<i>Status: Patented, Pending, Abandoned</i>

**Declaration for Patent Application (cont'd.)**Atty. Docket No. 00-046-NSC

I hereby appoint the following registered practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Ernie L. Brooks, Reg. No. 26,260; James A. Kushman, Reg. No. 25,634; David R. Syrowik, Reg. No. 27,956; Mark A. Cantor, Reg. No. 30,614; Ralph M. Burton, Reg. No. 17,748; Robert C.J. Tuttle, Reg. No. 27,962; Earl J. LaFontaine, Reg. No. 30,766; Ronald M. Nabozny, Reg. No. 28,648; Thomas A. Lewry, Reg. No. 30,770; John E. Nemazi, Reg. No. 30,876; Kevin J. Heintz, Reg. No. 29,805; William G. Abbatt, Reg. No. 31,936; Donald J. Harrington, Reg. No. 17,427; Timothy G. Newman, Reg. No. 34,228; Frederick M. Ritchie, Reg. No. 18,669; Robert C. Brandenburg, Reg. No. 29,048; A. Frank Duke, Reg. No. 20,937; John M. Halan, Reg. No. 35,534; Jeffrey M. Szuma, Reg. No. 35,700; James R. Ignatowski, Reg. No. 26,741; Frank A. Angileri, Reg. No. 36,733; William G. Conger, Reg. No. 31,209; Sangeeta G. Shah, Reg. No. 38,614; Christopher W. Quinn, Reg. No. 38,274; Robert C. Jones, Reg. No. 35,209; David S. Bir, Reg. No. 38,383; Konstantine J. Diamond, Reg. No. 39,657; James N. Kallis, Reg. No. 41,102; Hugo A. Delevie, Reg. No. 32,688; Ralph E. Smith, Reg. No. 35,474; Michael S. Brodbine, Reg. No. 38,392; Jeremy J. Curcuri, Reg. No. 42,454; Mark D. Chuey, Reg. No. 42,415; John J. Ignatowski, Reg. No. 36,555; Pete N. Kioussis, Reg. No. 41,117; Stephanie M. Mansfield, Reg. No. 43,773; Mark E. Stuenkel, Reg. No. 44,364; Matthew R. Mowers, Reg. No. 44-956; Raymond J. Vivacqua, Reg. No. 45,369; Timothy R. Schulte, Reg. No. 29,013; Wayne P. Bailey, Reg. No. 34,289; Lawrence G. Almeda, Reg. No. 46,151; Ginta Kukainis, Reg. No. 46,082; Seth E. Rodack, Reg. No. 45,622; James W. Proscia, Reg. No. P-47,010.

Address all correspondence and telephone calls to Timothy R. Schulte  
at Storage Technology Corporation, One StorageTek Drive, MS-4309, Louisville, Colorado 80028-4309, (303) 673-5989.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Full Name of Sole or First Inventor** John W. Svenkeson

Inventor's signature John W. Svenkeson Date 10/13/00

Post Office Address 19151 Olinda Tr. N., Marine On St. Croix, MN 55047

Residence (same as above) Citizenship U.S.A.

**Full Name of Second Joint Inventor** John D. Hamre

Inventor's signature \_\_\_\_\_ Date \_\_\_\_\_

Post Office Address 318 Balsam Lane N., Plymouth, MN 55441

Residence (same as above) Citizenship U.S.A.

**Declaration for Patent Application (cont'd.)**

Atty. Docket No. 00-046-NSC

I hereby appoint the following registered practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Ernie L. Brooks, Reg. No. 26,260; James A. Kushman, Reg. No. 25,634; David R. Syrowik, Reg. No. 27,956; Mark A. Cantor, Reg. No. 30,614; Ralph M. Burton, Reg. No. 17,748; Robert C.J. Tuttle, Reg. No. 27,962; Earl J. LaFontaine, Reg. No. 30,766; Ronald M. Nabozny, Reg. No. 28,648; Thomas A. Lewry, Reg. No. 30,770; John E. Nemazi, Reg. No. 30,876; Kevin J. Heinel, Reg. No. 29,805; William G. Abbatt, Reg. No. 31,936; Donald J. Harrington, Reg. No. 17,427; Timothy G. Newman, Reg. No. 34,228; Frederick M. Ritchie, Reg. No. 18,669; Robert C. Brandenburg, Reg. No. 29,048; A. Frank Duke, Reg. No. 20,937; John M. Halan, Reg. No. 35,534; Jeffrey M. Szuma, Reg. No. 35,700; James R. Ignatowski, Reg. No. 26,741; Frank A. Angileri, Reg. No. 36,733; William G. Conger, Reg. No. 31,209; Sangeeta G. Shah, Reg. No. 38,614; Christopher W. Quinn, Reg. No. 38,274; Robert C. Jones, Reg. No. 35,209; David S. Bir, Reg. No. 38,383; Konstantine J. Diamond, Reg. No. 39,657; James N. Kallis, Reg. No. 41,102; Hugo A. Delevie, Reg. No. 32,688; Ralph E. Smith, Reg. No. 35,474; Michael S. Brodbine, Reg. No. 38,392; Jeremy J. Curcuri, Reg. No. 42,454; Mark D. Chuey, Reg. No. 42,415; John J. Ignatowski, Reg. No. 36,555; Pete N. Kioussis, Reg. No. 41,117; Stephanie M. Mansfield, Reg. No. 43,773; Mark E. Stuenkel, Reg. No. 44,364; Matthew R. Mowers, Reg. No. 44,956; Raymond J. Vivacqua, Reg. No. 45,369; Timothy R. Schulte, Reg. No. 29,013; Wayne P. Bailey, Reg. No. 34,289; Lawrence G. Almeda, Reg. No. 46,151; Ginta Kukainis, Reg. No. 46,082; Seth E. Rodack, Reg. No. 45,622; James W. Proscia, Reg. No. P-47,010.

Address all correspondence and telephone calls to Timothy R. Schulte  
at Storage Technology Corporation, One StorageTek Drive, MS-4309, Louisville, Colorado 80028-4309, (303) 673-5989.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Full Name of Sole or First Inventor** John W. Svenkeson

Inventor's signature \_\_\_\_\_ Date \_\_\_\_\_

Post Office Address 19151 Olinda Tr. N., Marine On St. Croix, MN 55047

Residence (same as above) Citizenship U.S.A.

**Full Name of Second Joint Inventor** John D. Hamre

Inventor's signature John D. Hamre Date 10/14/2000

Post Office Address 318 Balsam Lane N., Plymouth, MN 55441

Residence (same as above) Citizenship U.S.A.